

PROCESS STUFF
STORAGE ALLOCATION.

COMPASS - VER 2.

11/15/71 13.18.16.

PAGE 1

ADDRESS	LENGTH
0	56
	56

BINARY CONTROL CARDS.

IDENT	PROCESS
END	

BLOCKS	TYPE	ADDRESS	LENGTH
ABSOLUTE*	ABSOLUTE	0	171
PROGRAM*	LOCAL	0	56
EPROC	COMMON	0	130

ENTRY POINTS.

E.CPROC	0	E.STMSG	372	E.ARMIT	51
L.CPROC	227	L.STMSG	22	L.ARMIT	424
CPUIN	51	L.DLPROC	414	L.DSIT	101
E.CPUIN	227	L.DLPROC	414	E.DSIT	434
L.CPUIN	53	L.CLISS	51	L.DSPST	22
E.CPOUT	51	L.CLISS	414	E.DSPST	455
L.CPOUT	302	L.CLISS	420	L.DSPST	26
CLRMG	53	L.CLISS	420	E.DSPST	513
E.CLRMG	355	L.CLISS	420	L.DSPST	15
L.CLRMG	55	E.DPRED	420	E.DTNT	20
STMSG	51	L.DPRED	420	L.DTNT	11

EXTERNAL SYMBOLS.

E.ERROR	TOSPROC	NOPIND	NOROOM	ZBIGIX	DISASTR	WAKEOBJ	E.ECS
I.LOCK	FINDSUB	PATSON	BLMISS	ZBGPPT	SYSRET	SYNCHRO	
SCHED	CHKPTR	NOCP	CLMOT	ZBGPAR	PUTCAP	SYNCHL	
SYSFRET	NEGPT	MISSOR	BIGIX	ZBGPAR	CAPAB	ZDSYS	

IDENT PROCESS

* MOST ROUTINES CONCERNED WITH GLOBAL MANAGEMENT OF THE PROCESS DESCRIPTOR
 * (AS OPPOSED TO BEING CONCERNED WITH A PARTICULAR SUBPROCESS OR MAP)
 * ARE COLLECTED HERE. EXCEPTIONS ARE MANY, INCLUDING -

* STACK MANIPULATIONS IN SYSENT
 * ESTABLISHMENT OF PROCESS CORE IN SUBPROC

* SYSTEM ACTIONS HEREIN -

MAKPROC	CREATE A PROCESS
DELPROC	DELETE A PROCESS
DISPROC	DISPLAY A PROCESS
SETMESS	SET MESSAGE MECHANISM
CLRMESS	CLEAR MESSAGE MECHANISM
TIMIN	MOVE TIME INTO PROCESS
TIMOUT	MOVE TIME OUT OF PROCESS
SETIIB	SET INTERRUPT INHIBIT BIT
CLRIB	CLEAR INTERRUPT INHIBIT BIT
ARMINT	ARM INTERRUPTS FOR A GIVEN SUBP
DISARM	DISARM INTERRUPTS FOR A GIVEN SUBP
DSPST	DISPLAY STACK
DSSTE	DISPLAY STACK ENTRY
PTINT	SEND INTERRUPT

* EXTERNAL SUBROUTINES HEREIN -

* PRINT . SEARCH FOR INTERRUPT SUBPROCESS WITH PRIORITY
 OVER CURRENT SUBP

* INTERNAL SUBROUTINES HEREIN -

ARYTH	HANDLE USER ARITHMETIC ERRORS
CHIP	HANDLE USER CHIP CALL ERRORS

0

ERRNUMS	XTEXT
INTSYS	XTEXT
RECS	MACRO

* R

RECS	RJ
WECS	ENDM

* R

WECS	ENDM
TYPES	XTEXT

* R

ALOCSYM	XTEXT
ECSMAC	XTEXT

* R

CBLOCK	MICRO
--------	-------

EXT

EXT

1,*/EPROC/*
 E.ERROR,I,LOCK,SCHED,SYSFRET
 TOSPROC!FINDSUB

0

0

0

0

LIST
PROCSYN KTEXT

X

PROCESS SYMBOLS

	P. ₀ PARAML	EQU	40B	• SIZE OF PARAM BUF + SUBP CALL WORDS
36	P. ₀ APLL	EQU	P. ₀ PARAML-3	ACTUAL SPACE FOR PARAMETERS
4	P. ₀ SCHRL	EQU	4	• LENGTH OF SCHEDULER AREA
50	P. ₀ SCRL1	EQU	50B	• LENGTH OF PRINCIPAL SCRATCH AREA
6	P. ₀ SCR2L	EQU	6	• LENGTH OF ANOTHER SCRATCH AREA
372	P. ₀ PBUFL	EQU	250	• BLOCK PARAMETER BUFFER LENGTH
	*			
		ORG	0	
	P. ₀ SCR	BSS	P. ₀ SCR	• PRIMARY SCRATCH AREA
	P. ₀ SCR2	BSS	P. ₀ SCR2L	• ANOTHER SCRATCH AREA
	P. ₀ TEMP1	BSS		• . . MORE SCRATCH STORAGE
	P. ₀ TEMP2	BSS		
	P. ₀ TEMP3	BSS		
	P. ₀ TEMP4	BSS		
	P. ₀ TEMP5	BSS		
	P. ₀ TEMP6	BSS		
	P. ₀ TEMP7	BSS		
	*			
	P. ₀ PARAM	BSS	I	• AN XTRA STORE IN P.PARAM LANDS HERE
	P. ₀ PARAMC	EQU	P. ₀ PARAM	• ACTUAL PARAMETER AREA
	*		*	• END OF ACTUAL PARAM AREA (CLASS CODE)
			*	• AS OTHER WORDS GO HERE ON SUBP CALL

PROCESS READ ONLY DESCRIPTOR

G 2

P. ROHEAD 855

2

* NAMES OF PROCESS STATE FLAGS.
* FOR CROSS-REFERENCE PURPOSES ONLY.

PF.CP	EQU			• SOMETHING PENDING AT SWAPIN TIME
PF.CK	EQU			• UNCHAIN FROM EVENT CHANNEL
PF.CR	EQU			• SCHEDULED
PF.CD	EQU			• PENDING INTERRUPT
PF.CD	EQU			• PENDING DESTRUCTION OF PROCESS
PF.CM	EQU			• PSEUDO-PROCESS FLAG
PF.CP	EQU			• PROCESS IS IN CORE
PF.CV	EQU			• RESCHEDULED BY ARRIVAL OF EVENT
PF.CS	EQU			• DESCHEDULED FOR TIMER OUT
PF.CEE	EQU			• DESCHEDULED FOR ERROR ERROR
PF.CH	EQU			• DESCHEDULED CAUSE HUNG ON EVENT CHANNEL
4010	PF.CREA	EQU	4010B	• STATE BITS FOR NEW PROCESS
4200	PF.KILL	EQU	4200B	• FLAGS SET TO KILL PROCESS
6636	PF.SWPN	EQU	6636B	• FLAGS ALWAYS TURNED OFF AT SWAPIN
16	PF.DSCHD	EQU	0016B	• ANY OF THESE PREVENTS THE PROCESS
*				• FROM RUNNING

130

* BACK TO NAMING CELLS IN PROCESS DESCRIPTOR

P.SCHEn BSS P.SCHEDL

134

P.MSGEVC BSS

I . MESSAGE MECH - EVENT CHANNEL

135

P.MSGDAT BSS

I . MESSAGE MECH - EVENT TO SEND

136

P.TIMER BSS

I . PROCESS TIMER

11 P.PROCHO EQU

* P.RWHEAD . LENGTH OF PROC READ/ONLY DESCRIPTOR

137

P.RWHEAD EQU

* I

. USER TIME

140

P.USRTIM BSS

I

. SYSTEM TIME

141

P.SWPTIM BSS

I

. SWAP TIME

142

P.XPACK BSS

I

143

P.CLIST BSS

I

144

P.CTABLE BSS

I

145

PSTACK BSS

I

146

P.SUBPNT BSS

I

147

P.MAPSTN BSS

I

148

P.INTERR BSS

I

149

BSS

I

150

P.PROCROW EQU

I

151

P.LOCALC EQU

I

152

* USE

I

153

* PS.TEMP SET

I

P.PARAML/60

154

IFNE

I

P.PARAML/PS.TEMP*60,I

155

PS.TEMP SET

I

PS.TEMP+1 . ROUND UP

156

PS.MASKL EQU

I

PS.TEMP . LENGTH OF PARAMETER BIT MASK AREA

157

* SYMBOLIC NAMES OF THINGS IN THE SUBPROCESS DESCRIPTOR,

158

* WORDS

159

* SD.SIZE EQU

I

. SIZE OF SUPER DESC, EXCLUDING MAPS

160

SD.FIPT EQU

I

. FLAGS, INT DATUM, PTR TO NEXT SUBP

161

SD.CC EQU

I

. CLASS CODE WORD

162

SD.ESM EQU

I

. ERROR SELECTION MASK WORD

163

SD.CLIST EQU

I

. UNIQUE NAME, MOT OF LOCAL CLIST

164

SD.PTRS EQU

I

. MAX STACK, FATHER, MAPIN/CHAIN PTRS

165

SD.ORIG EQU

I

. ENTRY POINT, MAP AND C-LIST ORIGINS

166

SD.MAP EQU

I

. COMPILED AND LOGICAL MAP SIZE,

167

* SD.MAP BEGINS THE MAP AREA AND MUST COME AT THE END

I

. LOGICAL MAP POINTER

168

SD.RAFT EQU

I

. RA+FL,RA,SPACE LEFT IN COMP MAP

PROCSYM

				PROCSYM
0	*	FLAGS		PROCSYM
1	*			PROCSYM
2	SD.FINT EQU	0		PROCSYM
3	SD.FMPE EQU	1		PROCSYM
4	SD.FDIS EQU	2		PROCSYM
5		01101101		PROCSYM
6				PROCSYM
7				PROCSYM
8	*			PROCSYM
9	*	ENTRY POINT OFFSETS FOR DIFFERENT SUBPROCESS CALLS		PROCSYM
10	*			PROCSYM
11	P.CLLOFF EQU	0		PROCSYM
12	P.ERRORF EQU	1		PROCSYM
13	P.INTOFF EQU	2		PROCSYM
14	P.ITLOFF EQU	3		PROCSYM
15	*			PROCSYM
16	*	MAP NAMES = ALL ARE FOR CROSS-REFERENCE PURPOSES ONLY		PROCSYM
17	*			PROCSYM
18	MP.RAFI EQU	1		PROCSYM
19	MP.CNT EQU	2		PROCSYM
20	MP.CMAP EQU	3		PROCSYM
21		11010101		PROCSYM
22				PROCSYM
23				PROCSYM
24				PROCSYM
25	MP.SIZE EQU	0		PROCSYM
26	MP.FILE EQU	1		PROCSYM
27	MP.FADD EQU	2		PROCSYM
28	MP.CADD EQU	3		PROCSYM
29		01010101		PROCSYM
30				PROCSYM
31				PROCSYM
32	*			PROCSYM
33	*	THIS IS WHERE THE CLOCK FLAG IS SET		PROCSYM
34	*			PROCSYM
35	S.FNFLG EQU	2		PROCSYM
36				PROCSYM
37	*	THATS RIGHT; CELL 2 ABSOLUTE		PROCSYM
38	*			PROCSYM
39				PROCSYM
40	*	STACK FLAG NAMES:		PROCSYM
41	*	SF.PCQ1 AND SF.PCQ2 ARE FOR CROSS-REFERENCE ONLY		PROCSYM
42	*			PROCSYM
43	SF.II EQU	0		PROCSYM
44	SF.FINT EQU	1		PROCSYM
45	SF.PCQ1 EQU	2		PROCSYM
46	SF.PCQ2 EQU	3		PROCSYM
47		01010101		PROCSYM
48				PROCSYM
49				PROCSYM
50				PROCSYM
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373				PROCSYM

PROCESS STUFF
CREATE PROCESS - CHECK PARMs AND CALC LENGTH

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8

ORG

PARAMETER MNEMONIC₀

0
1
2
3
4
5
6
7
10
11
12
13
15
17
20
21
22
24
25
26

MP = ALLnC	BSS
MP = CAPT	BSS
MP = CHWN	BSS
MP = STACK	BSS
MP = CLSCD	BSS
MP = MAP	BSS
MP = COMP	BSS
MP = FL	BSS
MP = ENTRY	BSS
MP = CLIST	BSS
MP = FILE1	BSS
MP = FILE1	BSS
MP = CMAR1	BSS
MP = WDOCT1	BSS
MP = FILE2	BSS
MP = FILE2	BSS
MP = CMAR2	BSS
MP = WDCT2	BSS

2
1
1
1
2
1
1
1
1
1
1
1
1
1

CAPABILITY FOR ALLOCATION BLOCK
 INDEX IN FULL C-LIST TO RETURN CAP
 NUMBER OF CHAINING WORDS
 NUMBER OF ENTRIES IN THE STACK
 CLASS CODE FOR SUBPROCESS
 NUMBER OF ENTRIES IN THE SUPP MAP
 NUMBER OF WORDS RESERVED FOR COMPILED
 FIELD LENGTH OF SUBPROCESS
 ENTRY POINT OF SUBPROCESS
 C-LIST FOR SUBPROCESS
 FILE FOR 1ST SWAPPING DIRECTIVE
 FILE ADDRESS IF 1ST SWAPPING DIRECTIVE
 CORE ADDRESS FOR 1ST SWAPPING DIR
 WORD COUNT FOR 1ST SWAPPING DIRECTIVE
 FILE FOR 2ND SWAPPING DIRECTIVE
 FILE ADDR FOR 2ND SWAPPING DIRECTIVE
 CORE ADDR FOR 2ND SWAPPING DIRECTIVE
 WORD COUNT FOR 2ND SWAPPING DIRECTIVE

1

ERSCODE CBR00

卷之三

卷之三

LENGTH OF C-LIST
PTR TO STACK ORIG (REL \$1)
PTR TO SUBPROCESS TABLE (REL \$1)
TOTAL SIZE OF PROCESS DESC IN CM
LENGTH OF PROCESS IN CORE

卷一 6111000102

750

三六〇一〇

MAKPPB05 Sat

RÍTA-RÍDAM-AMB-ELISÉ

52 0110000001
53 54300

54200

43547

REC

1

READ MOT ENTR

43547

MXE

3

PROCESS STUFF
CREATE PROCESS - CHECK PARMs AND CALC LENGTH

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PPROC

PAGE 7

13112
11151
54 0311000275
15025
55 0110000001
56 54100
10611
5161000056

BX1 X1-X2 CHECK UNIQUE NAME
BX1 X5*X1
NZ X1+MKPROC44 ERROR..C-LIST GONE
BX0 -X5*X2 ECS ADDR OF C-LIST
RECS I
SA1 A6 LENGTH OF C-LIST
BX6 X1
SA6 B1+CLEN SAVE LENGTH OF C-LIST

*
* BEGIN CHECKING PARAMETERS AND ACCUMULATING
* CORE AND ECS LENGTH OF THE PARCESS
*

57 20101

LX1 I SPACE FOR LOCAL C-LIST BUFFER
* ADD SCRATCH LENGTH, DEAD CELL BEFORE
* ACTUAL PARAM AREA, ACTUAL PARAM BUFFER
* FIXED LENGTH PROCESS DESCRIPTOR, AND
* FULL C-LIST BUFFER
SX6 X1+p.R0HEAD-p.SCR+p.PROCRO+p.PROCRW+3

* X6 = CM LENGTH OF NEW PROCESS, BEING ACCUMULATED
*

60 5161000067

SA6 B1+STACK0 SAVE PTR TO STACK

* CHAINING WORDS
*

5111000071
61 7221777776
0332000253
62 7221777721
0322000267

SA1 B1+p.PARAM+MP.CHWD
SX2 X1-I
NG X2+MKPROC31 ERROR..LESS THAN 1 CHAINING WORD
SX2 X1-p.SCRL-p-I
PL X2,MK30X ERROR = TOO MANY CHAINING WORDS

* SPACE FOR CHAINING WORDS, ZERO WORD
AFTER CHAINING WORDS, CONSTANT LENGTH
PROCESS DESCRIPTOR

63 6221000044

SB2 X1+i+p.PROCRO+p.PROCRW

* B2 = ECS LENGTH OF NEW PROCESS, BEING ACCUMULATED
*

* STACK LENGTH
*

5131000072
64 7223777776
0332000254
65 20301
63232
36643
66 5161000060

SA3 B1+p.PARAM+MP.STACK
SX2 X2-I
NG X2+MKPROC31 ERROR..LESS THAN 1 STACK ENTRY
LX3 I
SB2 B2+x3 . ECS LENGTH + LENGTH OF STACK
IX6 X6*x3 . SIMILARLY FOR CM LENGTH
SA6 B1+SUBPT SAVE SUBPROCESS TABLE ORIGH

* LOGICALMAP SIZE+ SUBP DESC SIZE + DEAD WORDS AT END OF SUBP TABLE
*

5111000075
67 10211

SA1 B1+p.PARAM+MP.MAP
BX2 X1

PROCESS STUFF
CREATE PROCESS - CHECK PARMs AND CALC LENGTH

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FPROC

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20201
36251
70 7232000013

* LX2 1
* IX2 X2+X1 . 3*(NUM OF LOGICAL MAP ENTRIES)
* SX3 X2+I+S0.SIZE+SD.CC+1 . SPACE FOR MAP WITH LO AT THE E
* AND SUBP TABLE WITH XTRA CLASS
* AT THE END

71 63232 0332000255
36663

* NG X2,MKPROC35 ERROR..LESS THAN 2 MAP ENTRIES
* SB2 B2+X3
* IX6 X6+X3

* COMPILED MAP BUFFER

72 0332000256 5121000076
36662
63222
73 5161000061

* SA2 B1+P.PARAM+MP.COMP ADD COMPILED MAP BUFFER
* NG X2,MKPROC43 ERROR..NEG SPACE FOR COMPILED MAP
* IX6 X6+X2
* SB2 B2+X2
* SAK R1+DESCSIZ . SAVE TOTAL CM SIZE OF PROC RES

* CHECK SUBP FL

74 0331000257 5111000077
36641
75 7120000000 X
14222
76 5130000000 X
36222
77 0322000270 37262
5161000062

* SA1 B1+P.PARAM+MP.FL
* NG X1,MKPROC35 ERROR..NEGATIVE FIELD LENGTH DECLARED
* IX6 X6+X1
* SX5 SXENDSYS
* BX2 X2
* SA3 EXS.CMFL . CM AVAILABLE
* IX2 X3+X2 . LESS SYSTEM RESIDENT
* IX2 X6-X2
* PL X2,MK36X ERROR..FL TOO LARGE
* SA6 B1+CORELEN SAVE CORE SIZE

* ENTRY POINT

100 5121000100
7336777766
101 0333000260
37261
73220
102 0322000271

* SA2 B1+P.PARAM+MP.ENTRY
* SX2 X2+I
* NG X3,MKPROC36 ERROR..ENTRY POINT LESS THAN 3
* IX2 X2+X1
* SX2 X2
* PL X2,MK36X RROR = ENTRY .GT. FL

* PUT TOGETHER THE LOGICAL MAP ENTRIES IN P.S62 (LATER WRITTEN TO ECS IF ALL GOES WELL).

* MAKE DUMMY RUN ON MAP ENTRIES TO TEST FOR
* FILE BLOCKS PRESENT
* NOTE... REFER* DOES NOT DESTROY B2 WHERE WE HAVE
* THE TOTAL ECS SPACE NEEDED FOR THE PROCESS

* READ/WRITE ENTRY

103 5121000105 5111000104
0332000261
104 10611

* SA1 B1+P.PARAM+MP.FILEI+1 MAKE UP LOGICAL MAP ENTRY
* SA2 B1+P.PARAM+MP.FLADJ FOR 1ST SWAPPING DIRECTIVE
* NG X2,MKPROC38 ERROR..FILE ADDRESS NEGATIVE
* BX6 X1

PROCESS STUFF
CREATE PROCESS = CHECK PARM S AND CALC LENGTH

COMPASS - VER 2.
EPROC

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PROCESS STUFF
CREATE PROCESS - CHECK PARMs AND CALC LENGTH

COMPASS - VER 2.
FPROC 11/15/71 13.18.56.

PAGE 10

L 132 0743000277

LT B4,B3,MK43X ERROR - NOT ENOUGH SPACE FOR MAP

*
*
*

L 133 0331000273 5111000070
L 134 53221 5121000162
L 135 5022000002 37112 0331000137
L 136 37112 0305000274 0321000145

MKPROC1

SA1 B1+P,PARAM+MP,CAP1
NG X1,MKPROC37
SA2 B1+P,CLIST
SA2 B1*X2
IX1 X1-X2
NG X1,MKPROC2 JP IF INDEX IN LOCAL CLIST
SA2 A2+2
ZR X2,MK37X
IX1 X1-X2
PL X1,MKPROC1
ERROR - CAPAB INDEX TOO LARGE

TEST CAPABILITY INDEX

L 137 5151000067

6146000141

L 140 7170000001

0206000000 X

MKPROC2 SA5 B1+P.PARAM+MP.ALLOC+1

* B2 = ECS SIZE OF PROC FOR MAKEOBJ

SB4 MKPROC3 RETURN LINK

SX7 AT.PROC TYPE OF OBJECT TO ALLOCATE

JP =XMAKEOBJ CALL ALLOCATOR

*

MKPROC4

SA1 B1+P.PARAM+MP.CAPI CAPABILITY INDEX

BX6 X5 SAVF ECS ADDRESS

SA6 B1+ECSAD

SB5 =XCAPAB . WHERE MAKEOBJ LEFT THE CAP

SB6 MKPROC4 RETURN LINK

JP =XPUTCAP CALL +PUTCAP+

*

* INCREMENT REFERENCE COUNTS FOR REAL ON SWAPPING

* DIRECTIVES (AT P.SCR2 AND P.SCR2+3)

*

MKPROC4 SA4 B1+P.PARAM+MP.FL CALL +REFER+ FOR BUMP COUNTS

SX1 P.SCR2

1

2

SB3 MKPROC5

SB4 MKPROC6

SB6 MKPROC7

SB7 MKPROC8

JP =XREFER

SA4 B1+P.PARAM+MP.FL CALL +REFER+ FOR 2ND SWAPPING DIR

SX1 P.SCR2+3

1

2

SB6 MKPROC9

SB7 MKPROC10

JP =XREFER

SA1 B1+P.PARAM+MP.COMP

SX6 BX6 X1-X6 * COMPUTE AND SAVE SPACE LEFT IN

LX6 SA6 B1+COMPSPA * COMPILED MAP BUFFER

*

*

* BEGIN TO CONSTRUCT THE PROCESS

* PROCESS FIXED LENGTH DESCRIPTOR

*

* P-PROHEAD VFD 12/4010B.12/NUM.CH WD+1,18/MOT. INDEX:

* 18/PROC LEN IN CORE

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PROCESS STUFF
CREATE PROCESS - CONSTRUCT DESCRIPTOR IN ECS

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FPROC

PAGE 12

L 157 5120000001 x 73250
12662

L 160 20622 5121000042 12662

L 161 56610 * * P.ROHEAD+1 VFD 6/0,18/0,
* * * * * 18/FULL C-LIST TAB ORIG (REL P.ROHEAD),
* * * * * 18/LEN OF PROC VAR DESC = SD.SIZE+SD.CC
* * * * * +3*(NUM LOG MAP ENTRIES)+1
* * * * * +2*(NUM OF STACK ENTRIES)
* * * * * +COMP MAP SPACE

7221000044 20222 * SX2 X1+P.PROCR0+P.PROCRW+1 COMPUT C-TABLE ORIG
LX2 18 COMPUTE LENGTH OF PROC VARIABLE DESC

L 162 5131000057 5141000061 * SA3 B1+STACK0 . STACK ORIGIN
L 163 37443 12624 5564000001 SA4 B1+DESCSZIZ . TOTAL CM SIZE OF DESC
IX4 X4-X3 . LENGTH OF VAR DESC
BX6 X2+X4
SA6 A6+1

* * ZERO SCHEDULER DATA AND EXCHANGE PACKAGE

L 164 13666 6126000024 BX6 X6-X6
L 165 5066000001 MKPROC7 SB2 P.ESCHENL+14
6125777776 SA6 A6+1
SB2 B2+1
NE B0+BE+MKPROC7

* * SET EXIT MODE = 7

L 167 5076777763 43753 20763 MX7 3
LX7 36+3+12
SA7 A6-12

* * SET MA = B1+P.XPACK

L 170 5140000000 x 54370
73340 37343 * SA3 B1
SA4 =XS.CMFL
SX3 X3
IX3 X4-X3 . B1 FOR NEW PROCESS
SX7 X3+P.XPACK
LX7 36
SA7 A6-9 . STORE MA FOR NEW PROCESS

* * P.CLIST IS SET BY ENVIRON

* * * * * P.CTABLE VFD 6/0,18/LEN(LOCAL C-LIST)/2,18/LEN(FULL C-TABLE)
* * * * * 18/PTR TO ORIG OF FULL C-TABLE (REL B1)

*
 173 20422 5141000056
 SA4 B1+CLEN . RETRIEVE LEN(LOCAL C-LIST)
 LX4 I8
 SX4 I . C-TABLE SIZE = 1
 BX4 X6+X4
 LX6 I8
 SA3 R1+STACKO
 SX7 X3=1 STACK ORIGIN = 1
 BX6 X6+X7
 SA6 A6+2
 *
 * P-STACK VFD 6/0:18/LAST ENTRY PTR:I8/FIRST ENTRY PTR,
 * 18/TOP OF STACK POINTER ALL (REL B1)
 * B1+SUBPT . ORIG OF SUBPROCESS TABLE .
 * B1+STACKO : ORIG OF STACK
 * X4=2 : END OF STACK
 * LX6 I8
 * BX6 X6+X2 . TOP OF STACK = ORIGIN OF STACK
 * LX6 I8
 * BX6 X6+X2
 * SA6 A6+1
 *
 * P-SURPDT VFD 6/0:18/PTR TO NEXT SUBP TO CREATE
 * 18/PTR TO ORIG OF SUBP TABLE BOTH REL B1
 * 18/NUMBER OF SUBPS
 *
 * B1+DESCSIZ
 * X2=1-S0+CC
 * LX6 I8
 * BX6 X6+X4
 * LX6 I8
 * MX7 59
 * IX6 X6=X7
 * SA6 A6+1
 *
 * WRITE OUT PROCESS READ ONLY AND READ WRITE DESCRIPTOR
 *
 * SAI B1+ECSAD RECEIVED ECS ADDRESS
 * BX0 X1
 * SA0 B1
 * WECSS P.MAPSIN-P.ROHEAD
 *
 * LEAVE ALL TEMPORIES AND CHAINING WORDS ZERO
 * BY INCREMENTING ECS ADDR (ALLOCATOR ZEROS ECS)
 * (SKIP DOWN TO VAR LEN DESC IN ECS)
 *
 * SA2 B1+P.PARAM+MP.CHWD . NUMB OF CHAINING WORDS
 * SX2 X2+1+P.PROCRO+P.PROCRW . +0 WORD +FIXED LEN 0
 * IX0 X0+X2
 *
 * STACK WITH ONE ENTRY

	43601	MX6	I	• SET INTERRUPT INHIBIT FOR ROOT	
	10746	SX7	X6		
	20704	LX7	SF, PC01-SF, IT		
I	211 12767	BX7	X6+X7	• SET ADDRESS TO EXECUTE IN STACK	
	20770	LX7	60-SF, PC01		
I	212 721177774	SA1	B1+P, PARAM+MP, ENTRY		
	15671	SX1	X1-P, ITLOFF		
I	213 5111000060	BX6	X7+X1	• PUT P-COUNTER IN STACK	
	15671	SA6	B1	• SET FIRST WORD OF STACK	
I	214 12661	SA1	B1+SUBPT	ORIG OF SUBP TABLE	
	5066000061	BX6	X1		
		LX6	18		
		BX6	X6+X1	TOP OF PATH = CURRENT SUBPROCESS	
		SA6	A6+1	• SET SECOND WORD OF STACK	
		* WRITE OUT STACK ENTRY			
I	215 0120000002	WECS	I		
I	216 5151000072	SA5	B1+P, PARAM+MP, STACK	INCREMENT ECS ADDR TO	
	20561	LX5	I	SUBPROCESS TABLE (SKIP OVER REST	
I	217 36005	SX5	X5	OF STACK)	
		IX0	X0+X5		
		* SUBPROCESS TABLE ENTRY			
		* WORD SD-FIPT			
		VFO	6/FLAGS = 0		
			18/INTERRUPT DATUM = 0		
			18/C-LIST LENGTH		
			18/NEXT PTR = SD.SIZE+COMP.MAP+3*LOG.MAP+1		
	5121000056	SAR	B1+CLEN	• RETRIEVE LENGTH OF CLIST	
	10622	BX6	X2		
I	220 20622	LX6	18		
	5121000075	SA2	B1+P, PARAM+MP, MAP		
	73120	SX1	X2		
I	221 20101	LX1	X1		
	36221	IX2	X2+X1		
	5111000076	SA1	B1+P, PARAM+MP, COMP		
I	222 36221	IX2	X2+X1		
	7222000071	SX2	X2+SD.SIZE+1		
I	223 5161000000	BX6	X6+X2		
		SA6	B1+SD.FIPT		
		* WORD SD-CC			
		NAME OF SUBPROCESS (CLASS CODE)			
I	5111000074	SA1	B1+P, PARAM+MP, CLSCH+1		
I	224 10611	BX6	X1		
	5161000061	SA6	B1+SD.CC		
		* WORD SD-ESM			
		ERROR SELECTION MASK			
	43601	MX6	I	• SET MASK FOR ERROR ERROR ONLY	

PROCESS STUFF
CREATE PROCESS - CONSTRUCT DESCR IN ECS

COMPASS - VER 2.
FPROC

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I 225	20662		LX6	60-E.NOERR
	5161000002		SA6	B1+SD.ESM
		*		UNIQUE NAME, NOT OF LOCAL C-LIST
I 226	5111000102	10671	SA1	B1+P.PARAM+MP.CLIST+I
I 227	5161000003		BX6	X1
			SA6	R1+SD.CLIST
		*		VFD 6/0
		*		18/MAX STACK PTR (REL B1)
		*		18/FATHER PTR (0 FOR ROOT)
		*		18/MAPIN LIST PTR (REL B1) (0 FOR NOW)
I 230	7261777773	5111000060	SA1	B1+SUBPT . START OF SUBP TABLE IS ALMOST END OF STA
		20644	SX6	X1-4 . NEXT TO LAST STACK ENTRY IS THE LIMIT
I 231	5161000004		LX6	36
			SA6	R1+SD.PTRS
		*		VFD 6/0
		*		18/ENTRY Point
		*		18/MAP ORIG (0 FOR ROOT)
		*		18/ C-LIST ORIG (0 FOR ROOT)
I 232	10611	5111000100	SA1	B1+P.PARAM+MP.ENTRY
		20644	BX6	X1
		5161000005	LX6	36
			SA6	R1+SD.ORIG
		*		VFD 1/MAP ON-OFF FLAG (0=ON)
		*		5/0
		*		18/COMPILED MAP SIZE
		*		18/NUM OF LOGICAL MAP ENTRIES
		*		18/BTR TO LOG MAPS REL THIS WORD
I 233	5111000076	10671	SA1	B1+P.PARAM+MP.COMP
		20622	BX6	X1
I 234	5121000075	10642	LX6	18
			SA2	B1+P.PARAM+MP.MAP
I 235	7211000062	20622	BX6	X6+X2
		10641	LX6	18
I 236	5161000006		SX1	X1+2
			BX6	X6+X1
			SA6	B1+SD.MAP
		*		VFD 6/0
		*		18/RA+FL (REL B1)
		*		18/RA (REL B1)
		*		18/SPACE LEFT IN COMPILED MAP
I 237	5121000077	5111000061	SA1	B1+DESCSIZ . ERA
		36672	SA2	B1+P.PARAM+MP.FL
			IX6	X1+X2 . RA+FL

PROCESS STUFF
CREATE PROCESS - CONSTRUCT DESC IN ECS

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FPROC

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		20622	LX6	18	
I	240	12661	BX6	X6+X1 . RA	
		20622	LX6	18	
		5111000064	SA1	R1+COMPSPA	. RETRIEVE SPACE LEFT
I	241	12661	BX6	X6+X1	
		5161000067	SA6	R1+SD,RAFL	
I	242	0120000010	*		WRITE OUT SUBP TABLE
			WECS	SD.SIZE	
			*		* THE COUNTS ON THE COMPILED MAP ARE TO FORCE COMPILE
			*		* SKIP OVER THE COMPILE MAP TO WRITE THE LOGICAL MAP
I	243	5111000076	SA1	R1+P,PARAM+MP,COMP	
		7211000010	SX1	X1+SD.SIZE	SKIP COMP MAP AND SUBP DESC
I	244	36001	IX0	X0+X1	
			*		
			*		* WRITE OUT SWAPPING DIRECTIVES
			*		
I	245	0120000006	SA0	R1+P,SCR2 . ADDR WHERE MAPS WERE SET UP	
		5101000050	WECS	MP.SIZE+MP.SIZE	
I	246	5111000075	SA1	R1+P,PARAM+MP,MAP	INCREMENT ECS ADDR TO END OF LOG MAP
		16211	BX2	X1	
		20201	LX2	1	
I	247	36221	IX2	X2+X1	
		36002	IX0	X0+X2	
			*		
			MX6	60	-0 FOR END OF MAP
			SA6	A0	
I	250	012000001	WECS	1	
I	251	020000000 X	JP	EXSYSRET	
			*		
			*		
			40		DISASTER/MISC ERRORS
			*		
I	252	0100000000 X	MKPROC20	RJ	5,XDISASTR
I	253	6140000003	MKPROC30	ERROR	3,XNEGPAR
I	254	6140000004	MKPROC31	ERROR	4,XNEGPAR
I	255	6140000006	MKPROC33	ERROR	6,XNEGPAR
I	256	6140000007	MKPROC43	ERROR	7,XNEGPAR
I	257	6140000010	MKPROC35	ERROR	8,XNEGPAR
I	258	6140000011	MKPROC36	ERROR	9,XNEGPAR
I	259	6140000014	MKPROC28	ERROR	12,XNEGPAR
I	260	6140000015	MKPROC39	ERROR	13,XNEGPAR
I	261	6140000016	MKPROC40	ERROR	14,XNEGPAR
I	262	6140000020	MK38X	ERROR	16,XNEGPAR
I	263	6140000021	MK39X	ERROR	17,XNEGPAR

PROCESS STUFF
CREATE PROCESS - CONSTRUCT DESCRIPTOR IN ECS

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266	6140000022	MK40X	ERROR	18,NEGPAR
267	6140000003	MK30X	ERROR	3,BIGPAR
270	6140000010	MK35X	ERROR	8,BIGPAR
271	6140000011	MK36X	ERROR	9,BIGPAR
272	6140000000	MKPROC41	ERROR	0,BIGT
273	6140000002	MKPROC37	ERROR	2,NEGX
274	6140000002	MK37X	ERROR	2,BIGTX
275	6140000012	MKPROC44	ERROR	10,CLMOT
276	6140000016	MKPROC42	ERROR	14,BLMISS
277	6140000000	MK43X	ERROR	0,NOROOM
300		ENDECS	CPROC	

* MACROS

*
* CHKMOT WHERE,ERROR ; CHECKS MOT ENTRY. FINDS
* * * * * UNIQUE NAME/MOT AT ↑WHERE↑ AND EXITS
* * * * * TO ↑ERROR↑ IF UNIQUE NAMES DONT MATCH

* LEAVES

*
* X0:=39 BIT MASK
* X1:=CONTENTS OF ↑WHERE↑
* Y2:=B70 PART OF MOT ENTRY
* A0:=#B1

* CHKMOT MACRO

WHERE,ERROR

S1
SX0
SA0
RECS
SA2
MX0
BX3
BX3
NZ
BX2
ENDM

S1+WHITE

X1

Y1

40

39

X1-X2

X3-X0

X3,ERROR

=X0*X2

* THIS DUMMY MACRO MARKS WHERE AB STUFF SHOULD BE LOCKED IN THE
* TWO-PP CASE

* RESERVE MACRO
RESERVE ENDM

* THIS ONE MARKS THE UNLOCK SPOTS

RELEASE MACRO
RELEASE ENDM

* SET WHERE,REG ; SETS LOCKS (LIKE I-LOCK)
* * * * * TO CONTENTS OF REG

* SETLOCK MACRO
SX6
SA6
WHERE
ENDM

* FAMILY ABLK,OBJ,ERROR ; CHECKS IF THE MOT PTR FOR
* * * * * ABLK IS THE SAME AS THE ONE IN
* * * * * THE ALLOCATOR HEADER WORD FOR OBJ.
* * * * * ABLK:=PTR REL:=#1 FOR WD 1 OF CAPABILITY FOR ALLOCATION
* * * * * BLOCK
* * * * * OBJ:=PTR REL BT FOR WD 1 OF CAPABILITY FOR OBJ
* * * * * ERROR:=WHERE TO JUMP IN CASE OBJ DOESN'T BELONG TO ABLK

* INPUT

* A0:=ABS CM ADR OF 1 WD BUFFER

* OUTPUT

* X0:= TEST RESULT

* X1:= ALLOCATOR HEADER WORD FOR OBJ, NOT PTR, RIGHT JUSTIFIED
(BY A LEFT CIRCULAR SHIFT)

* X2:=WD 1 OF AB CAPABILITY

FAMILY	MACRO	ABLK,OBJ,ERROR
	SA1	R1+OBJ .. FETCH OBJ CAPABILITY
	SX0	X1 ISOLATE NOT PTR
	RECS	I
	SA1	A0 .. FETCH NOT FOR +OBJ+
	MX0	39
	BX0	=X0*X1
	SX1	IP,AWDS=1
	IX0	X0=X1 .. ADDR OF ALLOCATOR FIELD
	SA2	R1+ABLK
	RECS	I .. READ ALLOCATOR HEADER WORD
	SA1	A0
	LX1	6*I8 .. RIGHT JUSTIFY AB POINTER
	BX0	X1=X2
	SX0	X0 .. ISOLATE RESULT
NZ	X0,ERROR	
ENDM		

66
66
70
72

227
51 5151000072 0335000117
52 10755

* * CPUIN
* * MOVE CPU TIME INTO A PROCESS. CLEAR PF.S IF TIMER GOES POSITIVE.
* * CHECK IF THE PROCESS RECEIVING THE TIME SHOULD BE
* * RESCHEDULED.
* * USER X6 IS SET TO THE NEW VALUE OF P.TIMER
* *
* * ACTUAL PARAMETER AREA: MNEUMONICS

ORG	D.PARAM
BSS	.. C: AB
APT.P	.. C: PROCESS
APT.D	.. D: AMOUNT OF TIME TO MOVE
USE	*

* * REGISTER ALLOCATION

A0:=B1
B3:=1:=CONSTANT
X5:=NOT ENTRY OF IP2 (PROCESS)
X7:=IP3:=TIME MOVED / X7:=P.TIMER*IP3

* * TEMPORARIES USED : D.TEMP1

ENTRY	CPUIN
ECSCODE	CPUIN
CPUIN	
SA5	B1+APT.D .. FETCH TIME TO MOVE FOR ERROR CHECKING
NG	X5=CPUIN7 .. JP IF ERROR (TIME<0)
BX7	X5 .. X7:=IP3:=AMOUNT OF TIME TO MOVE

* * TEST ALLOCATION BLOCK : JUMP TO CPUIN IF AN ERROR

5111000047	APT.AR+1:CPUIN9
10022	X2 .. CHKNOT LEAVES X2:=PTR IN AR*'S NOT
57 6130000001	S53 . NICE CONSTANT
54675	SA0
60 0110000013	RESERVE
61 5010000010	RECS
5620000011	S41 .. READ AB
62 37412	AB.SIZE .. TIME AVAILABLE
37447	A0+AB.CPAVL
0334000122	SA2 .. TIME CONSUMED
63 36627	IX4 .. X1=X2
54620	IX4 .. X4-X7 .. TIME NOT COMMITTED LES PROPOSED DONATION
10460	X4:CPUINIT .. SORRY, NOT THAT MUCH TO GIVE
	IX6 .. X2+X7 .. NEW AR.CPUSD FIELD
	SA6 .. A2 .. PRESERVE ECS A(AB)
	BX4

* * DETERMINE IF PROCESS OK. JP TO CPUIN IF NOT

64 5111000071	APT.P+1:CPUIN10
10522	X2 .. X5:=PTR IN NOT ENTRY OF IP2 LEFT BY CHK
70 5111000071	FAMILY (APT.AR+1);(APT.P+1).CPUIN12 .. SEE IF IP2 IS A
	DESCENDENT OF IP1

```

* * SET I-LOCK AND BEGIN TO MOVE TIME
*
SETLOCK    I-LOCK,B3
SX1         P-TIMER=P-ROHEAD
IX6         X5+X1 .. X5=ECS ADDR OF PROCESS
RECS
SA3
I
A0
IX7         X3+X7 .. X3=OLD P-TIMER X7=NEW P-TIMER
SA7         A0+0
WECS
SA7         R1+P-XPACK+15 : SEND RESPONSE TO USER

* * UPDATE ALLOCATION BLOCK. PERFORM RESCHEDULING LOGIC IF APPROPRIATE
*
SA0         R1+B3 .. CM A(AB)
BX0
WECS
RELEASE NG
ALLOCBLK
X3,CPUIN6 .. JP IF OLD P-TIMER >= LE. 0
ZR         X3,CPUIN6

* * CLEAR I-LOCK AND RETURN
*
CPUIN5    SETLOCK    I-LOCK,B0 .. I-LOCK:=B0
JP          SYSRET   .. RETURN

* * P-TIMER WAS NEG? IF NOW GT 0, UNSET TIMER OUT FLAG AND
* SEE ABOUT RESCHEDULING THIS PROCESS
*
CPUIN6    NG         X7,CPUIN5 .. DON'T BOTHER RESCHEDULING IF NEW P-TIME
ZR         X7,CPUIN5 .. LE. 0
BX0         X5 .. ADDRESS OF P-ROHEAD
MX3
LX3         60-PF.S .. POSITION MASK TO PF.S
RECS
SA1         I .. PETCH P-ROHEAD FROM ECS
A0
BX6         -X3*X7 .. CLEAR PF.S

* * SEE IF THE PROCESS GETS RESCHEDULES
*
S87         CPUIN5 .. RETLINK
EQ         CHKSCHD

* * ERRORS
*
CPUIN7    ERROR     I-NEGPAR .. NEGATIVE TIME
CPUIN9    ERROR     I-MISSOB .. AB GONE
CPUIN10   ERROR     2-MISSOB .. PROCESS GONE
CPUIN11   ERROR     3-NOCPP .. AB CAN'T MOVE THAT MUCH TIME
CPUIN12   ERROR     2-FATSON .. IP2 NOT DESCENDED FROM IP1

```

PROCESS STUFF
CPUIN: MOVE CPU TIME TO A PROCESS

COMPASS - VER 2.
FPROC

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ENDECS CPUIN

*

* CPOUT

*

MOVE CPU TIME FROM A PROCESS TO AN AB

CHECK TO SEE IF P.TIMER GOES NEGATIVES IF SO, ONLY MOVE ENOUGH TIME TO MAKE P.TIMER EQUAL TO ZERO
USER X6 IS SET TO NEW VALUE OF P.TIMER IN IP2

ACTUAL PARAMETER AREA (REL #I+8,PARAM)

APT·AB C: AB (WD 0)
+1 (WD 1)
APT·P C: PROCESS (WD 0)
+1
APT·D D: DATUM : AMOUNT OF TIME TO MOVE

REGISTER ALLOCATION

X4:= IP1 :: MOT OF AB
X5:= IP2 :: MOT OF PROCESS
X7:= IP3 :: AMT OF TIME TO MOVE / X7:NEW VALUE OF P.TIMER
82:= 1 :: CONSTANT

ENTRY CPOUT
ECSCODE CPOUT
SA:= RI+APT.D
PS
NC X4,CPOUT? :: ERROR? TIME⁰
SB2 ? :: REGISTER ALLOCATION
BX7 X4 :: X7:= IP2 := AMT TO MOVE, REGISTER ALLOC

CHECK MOT OF THE AB. BRANCH TO CPOUTB IF THERE IS AN ERROR

CHKMOT APT·AB+1+CPOUTB

X1:= WD 1 OF AB CAPABILITY
X2:= MOT OF AB
A0:=#81
X0:= 39 BIT MASK

BX4 X2 :: REGISTER ALLOCATION. X4:=MOT ENTRY OF A

CHKMOT P·PARAM*3,CPUING :: CHECK MOT OF PROCESS. JP TO
IF THERE IS AN ERROR

BX5 X2 :: REGISTER ALLOCATION (X2 := PTR IN MOT
ENTRY FOR PROCESS
FAMILY (APT·AB+1),(APT·P+1),CPOUT10 :: DOES THE PROCESS
BELONG TO THE AB? JP CPOUT10 IF NOT.

FIND OUT IF IP2 = CURRENT PROCESS IN THIS CPU

302
51 5141000072
52 0000000000
53 0334000120
54 10744 6126000001

5111000047

10422

61 5111000071

10522

65 5111000071

PROCESS STUFF
CPOUT: MOVE CPU TIME FROM A PROCESS TO AN AB

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		*	IDPROC	APT.P+1 .. ADDR OF WD 1 OF A CAPABILITY FOR A PROC
	74650	*	SETLOCK	I.LOCK,B2 .. SET I.LOCK (=B2:=1)
	0301000106	*	ZR	X1,CPOUT4 JP IF SAME PROCESS
		*	* IF PROCESS DOESNT REALLY HAVE X4 AMOUNT OF TIME TO RELEASE, X4 WILL BE * TO CURRENT VALUE OF P.TIMER ALL THE TIME IN THE PROCESS	
D	76 7110000010		SKI	P.TIMER=P.ROHEAD .. OFFSET FROM P.ROHEAD
L	77 0110000001	36051	IX0	X6+X1 .. ABS ECS ADDR. OF P.TIMER
L	100 54100	54100	RECS	I
L	101 54600	37617	SA1	A0
L	102 0120000001	0334000104	IX6	X1-X7 .. X6=NEW P.TIMER
L	103 0200000112		NG	X6,CPOUT3 .. WHOOPS DIDNT HAVE THAT MUCH TIME AFTER
		CPOUT2	SA6	A0
			WECS	I .. RESET ECS VERSION OF P.TIMER
			JP	CPOUT6 .. GO FIX UP AB
		*	* P.TIMER WENT NEGATIVE. RESET X4 TO CURRENT VALUE OF P.TIMER	
L	104 76600	10711	CPOUT3	SX6 B0 .. P.TIMER MUST = ZERO
L	105 0200000102	5660000000	BX7 X1	.. AB RECEIVED AMT. OF TIME IN OLD P.TIME
			SA6 A0+0	
			JP CPOUT2	
		*	* IP2=CURRENT PROCESS. MUST RESET P.TIMER IN CORE	
L	106 5111000136	37617	CPOUT4	SA1 B1+P.TIMER .. X6=NEW VALUE OF P.TIMER
L	107 0336000104	54610	IX6 X1-X7	.. WHOOPS TOO MUCH
L	110 10055	54600	NG X6,CPOUT3 .. CORE VERSION OF P.TIMER	
L	111 0200000102	7110000010	SA6 B1	.. PREPARE TO MODIFY ECS VERSION
L		36001	SA6 A0	.. X6=NOT ENTRY OF PROCESS
L			BX0 X5	P.TIMER=P.ROHEAD
L			SX1 X0+X1	.. ECS ADDRESS OF P.TIMER
L			IX0 CPOUT2	.. GO RESET ECS VERSION OF P.TIMER
		*	* RESET THE AB	
L	112 7110000010	36074	CPOUT6	SX1 AB.CPAVL ..
L	113 0110000001		IX0 X1+X4	.. ECS ADDRS OF TIME AVAILABLE
L	114 54100	5474	RECS I	
L	115 0120000001	54740	SA1 A0	
L	116 76600		IX7 X1+X4	.. INCREMENT CPU TIME AVAILABLE
L	117 5161000162		SA7 A0	
L			WECS I	I.LOCK,B0 .. CLEAR I.LOCK (=B0)
L			SETLOCK B1+P.XPACK+16	.. USER X6 := NEW VALUE OF P.TIM
L			SA2 ON IP2	ON IP2
L			JP SYSRET	
L			ERROR 3.NEGPAR	.. TIME TO MOVE < 0
L	120 6140000003	0200000000 X		

PROCESS STUFF

CPOUT: MOVE CPU TIME FROM A PROCESS TO AN AB

121 6140000001
122 6140000002
123 6140000002
124

CPOUT8 ERROR
CPOUT9 ERROR
CPOUT10 ERROR
ENDEC\$

#

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1,MISSOB .. AB GONE
2,MISSOB .. PROCESS GONE
2,FATSON .. PROCESS NOT SON OF AB
CPOUT

CLEAR MESSAGE MECHANISM IN A PROCESS

†IFF† MISSOR(IPI) †THEN† ERROR RETURN †FI†
 SETLOCK(I.LOCK)
 ZEROMSGMECH(IPI)
 CLEARLOCK(I.LOCK)
 RETURN
 †END†

355

ENTRY
ECSCODE CLRMSG
CLRMSG

51 5111000067

CLRMG CHKMOT

P.PARAM*1,CLRMG3 .. CHECK MOT OF PROCESS. JUMP
 TO CLRMG3 IF THERE IS AN ERROR
 X6=X6 .. PREPARE TO CLEAR MESSAGE MECHANISM
 P.PARAM*1 .. WD 1 OF A CAPABILITY FOR IPI (PROCESS)
 CHECK IF IPI = CURRENT PROCESS IN THIS CPU
 X0†=X2
 X1†= RESULT OF COMPARISON (0=>IDENTICAL PROCESS)
 X2†= P.ROHEAD OF ECS VERSION OF PROCESS

13660

BX6 IDPROC

55 7160000001
56 5161000135

SETLOCK
SA6

I.LOCK,1
B1+P.MSGDAT

57 54600
5060000001

CLRMG2

SA6
SA6

A0 ZERO THE ECS VERSION
 A0+1 THIS CODE ASSUMES THAT P.EVCDAT DIRECTLY
 FOLLOWS P.FVCDAT IN ECS.

60 7110000006
36001

*

SX1
IX0
WECS

P. MSGEVCDAT-P. ROHEAD
X0+X1 .. READ ECS ADDRESS

61 0120000002
62 5160000000 X
63 0000000000
64 0200000000 X
65 6140000001
66

*

SAS
PS
JP
ERROR
ENOECS

I.LOCK .. CLEAR I.LOCK
SYSRET
I.MISSOB
CLRMG

*
* STMSG
* SET THE MESSAGE MECHANISM IN A PROCESS.
* THE MESSAGE MECHANISM IS THE SECOND WORD OF A CAPABILITY FOR A
* CHANNEL AND A 60 BIT DATUM
*
* +IF+ MISSOB(IP2), +THEN+ ERROR RETURN +FI+
* +IF+ MISSOB(IP1) +THEN+ CLEARLOCK(I-LOCK), ERROR RETURN +FI+
SETMSGMECH(IP1)
CLEARLOCK(I-LOCK)
RETURN
*
* ACTUAL PARAMETER AREA RELATIVE TO P.PARAM
*
* +0 C: PROCESS (WD 0)
* +1 (WD 1)
* +2 C: EVENT CHANNEL (WD 0)
* +3 (WD 1)
* +4 D: DATUM
*
* X6:= MOT OF IP2 (EVCH)
X5:= IP3:= DATUM
*

372

L 51 5111000071		ENTRY ECSCODE	STMSG
	10622		STMSG
L 55 5111000067		CHKMOT	P.PARAM*3,STMSG3 .. CHECK UNIQUE NAME OF EVENT CHANNEL. JP TO STMSG3 IF AN ERROR
L 61 5151000072		BX6	X2 .. SAVE MOT ENTRY OF EVCH
		CHKMOT	P.PARAM*1,STMSG4 .. CHECK UNIQUE NAME OF PROCESS.
		SAS	JP TO STMSG4 IF AN ERROR.
			R1+P.PARAM*4 .. X5:=IP3:=DATUM
			(IP2=CURRENT PROCESS IN CPU)
L 62 5060000001	54650 10655	SA6	A0 .. XMIT EVCH
	71A0000001	BX6	X5
		SA6	A0+1 .. XMIT DATUM
		SETLOCK	I-LOCK,1
L 64 36002	7120000006	SX2	P.MSGEVC=P.ROHEAD
L 65 0120000002		IX0	X0+X2 ABS ECSC ADDR
L 66 76600		WECS	
L 67 0000000000		SETLOCK	I-LOCK,B0 .. CLEAR I-LOCK
L 70 0200000000	*	RS	
L 71 6140000001		JP	SYSRET
L 72 6140000002		ERROR	1,MISSOB .. PROCESS GONE FROM MOT
L 73		ERROR	2,MISSOB .. EVNT CHANNEL GONE FROM MOT
		ENDEC	STMSG

PROCESS STUFF
DELETE PROCESS

414
51 0000000000
52

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ECS CODE DLPRO
PS
ENDEC S DLPRO

PROCESS STUFF

CLIIB--CLEAR INTERRUPT INHIBIT BIT ON TOP OF STACK (

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29

*
* CLIIB
* CLEAR INTERRUPT INHIBIT BIT ON TOP OF STACK (TOS)
*
* PARAAMETERLESS ACTION
*

* ZEROBIT(SF,II,TOS)
* \uparrow GOTO \uparrow TOSPROC
*

* NOTE: TOSPROC CHECKS IF THERE IS A PENDING INTERRUPT.
* IF SO, THE INTERRUPT IMMEDIATELY STRIKES. SEE TOSPROC FOR
* FURTHER DETAILS
*

415

ENTRY
ECSCODE CLIIB
CLIIB

MX0 I .. MASK
SA1 R1+P,STACK .. FETCH PTR TO TOS
SA2 X1+B1 .. FETCH W0 0, TOS
LX0 60-SF,II .. POSITION MASK TO SF,II
BX6 ZX0*X2 .. CLEAR SF,II ON TOS
SA6 A2+0 .. UPDATE TOS ENTRY
JP TOSPROC .. GO CHECK FOR PENDING INTERRUPTS
SEE NOTE ABOVE AND COMMENTS IN TOSPROC

54

ENDECS CLIIB

PROCESS STUFF
STIIB--SET INTERRUPT INHIBIT BIT ON TOP OF STACK

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420

51 43001
20074

5111000164

52 53211
12620
54620
53 0200000000 X
54

*
* STIIB
* SET INTERRUPT INHIBIT BIT ON TOP OF STACK
*
*
* PARAMETERLESS ACTION
*
* ENTRY ECSCODE STIIB
* STIIB MX0 LX0 I
* 60-SF,II .. POSITION MASK TO SF,II
* SA1 B1+P,STACK .. FETCH PTR TO CURRENT TOP OF
* STACK
* SA2 B1+X1 .. FETCH W0,0 OF TOS ENTRY
* BX6 X2+X0 .. SET SF,II
* SA6 A2 .. STORE ALTERED STACK ENTRY
* JP SYSRET
* ENDECS STIIB

PROCESS STUFF
DISPLAY PROCESS DESCRIPTOR

423
51 0000000000
52

ECSCODE DPR00
PS
ENDECS DPR00

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```

*
* ARMIT
*
* ARM INTERRUPTS FOR A GIVEN SUBPROCESS
*
* IF NOT FIND(IP1) THEN ERROR RETURN
* ELSE CLEARBIT(SD.FDIS,IP1) +FI
RETURN

424
ENTRY ECSCODE ARMIT ARMIT
*
51 6170000053 SB7 ARMIT .. RETURN FOR +FINDSUB
5137000067 S1+P.PARAM+1 .. WD 1 OF CC (FOR +FINDSUB)
52 0200000000 X JP ARMIT2 .. JP IF NOSUCH SUBPROCESS

DO ERROR RETURN IF NECESSARY (NO SUCH SUBPROCESS)

53 64250 SB2 A5 .. ADDRESS OF DESCRIPTOR(IP1)+SD.CC
64360 SB3 A6 .. SET BY FINDSUB
0422000060 EQ R2,R3,ARMIT3 .. JP IF NOSUCH SUBPROCESS
*
54 43001 MX0 I
20072 LX0 60=SD.FDIS .. POSITION MASK
501577776 SA1 A5=SD.CC .. FETCH SD.FIPT IN SUBPROCESS DESCRIPTOR
55 15610 BX6 =X0*X1 .. TURN +OFF+ SD.FDIS (==>TURN OFF +INTERR
56 0000000000 PS
*
57 0200000000 X JP SYSRET .. RETURN
*
60 6140000001 ARMIT3 ENDECS +NOFIND .. NO SUCH SUBPROCESS AS IP1
61

```

```

* DISIT
* DISARM INTERRUPTS FOR A GIVEN SUBPROCESS
* PROCEDURE: DISIT(CC)
* BEGIN
*   IF NOSUBP(CC) THEN ERROR(NOFIND) FI
*   IF PENDING INTERRUPT(CC)
*     BEGIN
*       X:=PRINT(B), RETLINK
*       IF X = 9CC THEN
*         BEGIN
*           PCQ(TOS) := ABOUT TO EXECUTE
*           RETLINK := SYSRET
*           JP TOSBRO
*         END
*       ELSE E=RETURN FI
*     END
*   ELSE BEGIN
*     SETBIT(SD.FDIS, *C6*)
*     RETURN
*   END
* END

```

434

66

ENTRY
ECSCODE
CLASCODE EQU APT.A6

51 6170000053
5131000067
52 0200000000 V

DISIT SBT DISIT² DETERMINE IF IPI (*SD.CC+CLASCODE) REALLY EXISTS
SAB FINDSUB

* FINDSUB LEAVES A6=ADDR. REL.BI OF SUBPROCESS DESCRIPTOR (*SD.CC)
A6=DUMMY VALUE. IF A6=A6 THERE IS AN ERROR

53 64250
64360
0425000070

DISIT2 SBT A5
SBD A6
EQ B2,B3,DISIT² ., ERROR NOSUCH SUBPROCESS

* SAVE ADDR OF ISP. DESCRIPTOR IN B4. CORRECT THE SD.CC OFFSET.
* DETERMINE IF *CC HAS A PENDING INTERRUPT.

54 6142777776
54474
10044
55 20074
0330000061

SB4 R2=SD.CC
SA4 B1+B4
BX0 X4
LX0 60=SD.FINT
NG X0,DISIT³

* SIGH. THAT WAS EASY. SET SD.FDIS AND RETURN

PROCESS STUFF
DISIT--DISARM INTERRUPTS FOR A GIVEN SUBPROCESS

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```

*          43001
L 56 20072      MX0
          12604      LX0    I
          54640      BX6    60=SD.FDIS
          0000000000  SA6    X0*X4 .. ADD IT TO SD.FIPT (WD 0 OF SP DESCRIPTOR
          0200000000  x     A4    .. UPDATE DESCRIPTOR
          PATCH5      PS
          JP      SYSRET

*          TOO BAD.
*          AN INTERRUPT IS PENDING. IE ACC HAS PRIORITY. WE WILL LET
*          IT RUN (AFTER SETTING THE PCQ IN CURRENT TOS). OTHERWISE USER
*          WILL GET AN F-RETURN

L 61 6170000062  DISIT3  SB7    DISIT4
          0200000011  JP      PRINT
          *          B2=B4,DISIT6 .. SUBROUTINE RETURNS B2=0 IF NO PRIORITY
          *          INTERRUPT, B2=ADDR. OF SP DESCRIPTOR IF THERE IS A PRIORITY
          *          INTERRUPT.

L 62 0524000071  DISIT4  NE      B2,B4,DISIT6 .. JP TO AN F-RETURN IF PRIORITY
          *          INTERRUPT .NE. ACC
          *          RESET PCQ TO #ABOUT TO EXECUTE# FOR CURRENT TOS
          *
          5111000066
L 63 53111      SA1    B1+P,PARAM
          43001      SA1    B1+X1
          20070      MX0    I
          15610      LX0    60=SF,PCQ1
          15640      BX6    =X0*X1 .. MAKE SURE SF,PCQ1 AND SF,PCQ2
          43001      MX0    =I ARE BOTH ZERO, THEN SET SF,PCQ2
          20067      LX0    60=SF,PCQ2
          15640      BX6    =X0*X6
          43001      MX0    I
          12606      LX0    60=SF,PCQ1
          56610      BX6    X0*X6 .. AND SF,PCQ1=1
          SA6    A1    .. UPDATE TOS
          *
          *          PCQ(TOS) NOW INDICATES #ABOUT TO EXECUTE#. LET THE INTERRUPT RUN.
          *          WHEN THE INTERRUPT FINISHES, (AND IF THE STACK HASN'T
          *          BEEN DIDDLED) CONTROL WILL PASS AGAINTO THIS ACTION WHICH WILL
          *          THEN DISARM INTERRUPTS FOR ACC.

L 66 0000000000  PATCH5.5 PS
          67 0200000000  JP      TOSPROC

*          ERRORS-FRETURN
          *
          6140000001  DISIT5  ERROR  I,NOFIND
          0200000000  x   DISIT6  JP      SYSRET
          72      ENDECS  DISIT

```

ECS CODE DISPST
DISPST AND DISSEN ARE ECS ACTIONS TO DISPLAY THE STACK

DISPST

PARAMETERS :

D1: USPR CM ADDRESS

D2: WORD COUNT, MUST BE .GE. 4
THIS ACTION REFORMATS THE STACK ENTRIES AND TRANSFERS
THEM TO API. IF THE WORD COUNT IS TOO SMALL, THE TOP N
ARE TRANSFERRED, WHERE N=(A₂-1)/3. THE TOTAL NUMBER
OF ENTRIES IN THE STACK IS GIVEN AS THE FIRST WORD

DISPST

SB2	D1+P,XPACK+1
SA1	A1+B2
SA2	X6
AX1	D1+P,PARAM
NG	X3,NEUTR
AX2	X6
SA4	A3+B2
SB3	X2
SBS	X3
CE	D5,B2,BIGTA
SB4	X4+B5
SB4	B4+B2
GT	D4,B3,BIGTA
SB4	B4+B2
MX0	X4
SB7	X4
SA2	D1+U,STACK
NG	B7,N662P
SX3	X2+N+B2
AX2	B8
SX2	X2
I _X 2	X2
SX2	X2
AX6	D5,B2,X2
SB3	D5,B2,X2
SB3	D5,B2,X2
L7	D5,B2,X2
SB4	D5,B2,X2
SB3	D5,B2,X2
SB3	D5,B2,X2
SB7	D5,B2,X2
SAB	D5,B2,X2
SA1	B7+B2
NO	D1
SB6	SD,CC

*WORD COUNT

*END OF WRITE AREA

*END OF STACK

*STACK ORIGIN

*STACK LENGTH

*NUMBER OF ENTRIES

*SENSE NO ROOM FOR ENTIRE STACK

*END OF WRITE AREA

.. POSITION OF CLASS CODE IN SP DESCRIPTOR

DSS3

SA2	A1+B2	.. X2=WD0 OF STACK ENTRY
SA1	A2+B2	.. X1=WD1 OF STACK ENTRY
SB4	X1+B6	
SA3	B1+B4	.. X3=CC OF CURRENT SP
AX1	18	
SB4	X1+B6	

54414

SA4

R1+B4

.. X4=CC OF EOP SP

*
*STACK ENTRY VFD
* VFD
*
*

6/FLAGS; 18/F-RET COUNT; 18/IPLIST ADDR; 18/P-COUNT
24/0; 18/END OF PATH SP; 18/CURRENT SP

E 67 54662 10633
10644
54642 10611
E 70 54662 64462
0742000065
71 0000000000
72 0400000000
73 6140000001
74 6140000001
75 6140000002
76 6140000002
77

PATCH6

BX6 X3
SA6 A6+B2 .. XMIT CURRENT SP
BX6 X4
SA6 A6+B2 .. XMIT EOP SP
BX6 X1
SA6 A6+B2 .. XMIT WD 0 OF STACK ENTRY
SB4 A6+B2
LT R4,B3,DSS3 .. LOOP IF NOT THRU
PS
EQ SYSREQ
ERROR 1-NEGOT
BIG1A 1-BIGOPT
BIG2A 2-BIGOPT
NEG2P 2-NEGPAR
ENHEDCS DSPST

```
*  
* DSSTE  
*
```

DISPLAY SPECIFIC STACK ENTRY

```
*  
*  
* IF+ NEGPARM(IP1) +THEN+ ERROR +FI+  
* IF+ FLERROR(IP1+3) +THEN+ ERROR +FI+  
* IF+ NEGPARM(IP2) +THEN+ ERROR +FI+  
* IF+ BIGPARM(IP2) +THEN+ ERROR +FI+  
* WRITECORE( REFORMAT( STACKENTRY( IP2)),REFORMATSIZE)  
RETURN
```

ACTUAL PARAMETER AREA RELATIVE P.PARAM

```
*  
*  
* ORG P.PARAM  
BSS I .. DI CM ADDRESS  
BSS I .. DI WORD COUNT (FOR DSPST)  
EQU STK.WDCT .. DI STACK INDEX (FOR DSSTE)  
USE *
```

THE SAME REFORMATTING IS USED HERE AS IS USED IN DSPST
(DISPLAY STACK)

REGISTER ALLOCATION
B2:= 1 (CONSTANT)
B7:= -1 (CONSTANT)

```
*  
*  
* ENTRY DSSTE  
ECSCODE DSSTE
```

```
*  
* DSSTE SB2 I .. B2 := 1 (CONSTANT)  
SA1 B1+STK.CMAD .. CHECK OUT THE PROFFERED  
SB6 S . BUFFER  
SB7 DSSTE1  
EQ EXCHKPTR
```

DO ERROR CHECKING ON IP2. FETCH STACK ENTRY (IP2)

```
*  
* DSSTE1 SA1 B1+PSTACK .. X1 := C(PSTACK)=  
* VFD 6/0,18/STACKEND,18/STACKORIGIN,18/ST  
(RELATIVE B1)
```

```
*  
* SA2 B1+STK.INDX .. X2 := STK.INDX := IP2 + STACK  
INDEX RELATIVE TOS (TOP OF STACK)
```

```
*  
* IX2 X2+X2 .. DOUBLE INDEX (STACK ENTRIES ARE 2 WDS)  
NG X2,DeftE3 .. UP IF NEG STACK INDEX (ERROR)  
SK4 X1  
IX2 X4-X2 .. X2 := STACK INDEX REL. STACK ORIGIN  
SB4 X2+B1 .. PTR TO STACK ENTRY  
AX1 18 .. RIGHT JUSTIFY STACK ORIGIN  
SB5 X1+B1 .. PTR TO STACK ORIGIN  
LT B4,B5,DSSTE4 .. ERROR! INDEX TOO LARGE.  
REMEMBER THAT INDEX TOO LARGE (REL. TOS)
```

66

66

67

67

503

L 51 6120000001 5111000066
L 52 6160000003 6176000054
L 53 0400000000 X

L 54 5111000164

5121000067

L 55 36222 0332000044
73410

L 56 37242 63421 21152
63511

L 57 0745000065

PROCESS STUFF
DSSTE--DISPLAY SPECTIFIC STACK ENTRY

		*
		*
	561X0	
	10611	
60	54600	
	56242	*
		*
		*
61	5144000001	63421
		10644
62	21222	54662
		63421
63	10644	5144000001
		54662
64	6140000002	0500000000 X
65	6140000002	DSSTE3
66		DSSTE4

SA1
BX6
SA6
SA2

R4
X1
A0
R4+B2

X2+B1
B4+SD,CC
X4
A6+B2
18
SB4
SA4
BX6
SA6
JP
ERROR
ERROR
ENDECs

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==> PTR LESS THAN STACK ORIGIN

.. X1 := STACK ENTRY (WD.0) =
.. VFD 6/FLAGS,18/FRET,CNT,18/TP,ADR,18/P,CT

.. X2 := STACK ENTRY (WD.1) =
VFD 24/0,18/ENDPATH,SP,18/CURRENT,SP

.. ABS ADDRESS OF SUBPROCESS DESCRIPTOR
.. FETCH CLASS CODE (CC)

.. XMIT WD 1 (REFORMATTED), I= CC,CURRENT
.. RIGHT JUSTIFY END OF PATH

.. FETCH CC (END OF PATH)

.. XMIT CC OF END OF PATH

PROCESS STUFF
SEND INTERRUPT TO PROCESS

520
51 0000000000
52

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ECS CODE PTINT
PS
ENDECS PTINT

PROCESS STUFF
RESCHEDULE PROCESS IF ALL FLAGS KOSHER

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*
* THIS ROUTINE INSPECTS THE FLAGS PRESENTED IN X6 AND RESCHEDULES THE
* PROCESS IF THE OMENS ARE CORRECT. IT WRITES THE UPDATED P.ROHEAD
* INTO ECS

* AT ENTRY

* X6 = P.ROHEAD WORD FOR THE PROCESS
* X0 = ECS A(PROCESS)
* A0 = A(SUITABLE SCRATCH CELL)
* B7 = RETLINK
* I.LOCK MUST BE SET

* USES EVERYTHING BUT B1,B2,B3,B4,B6

0	10566	CHKSCHD	ENTRY	CHKSCHD
	20502		BX6	X6
	033E000006 *		LX6	PF.R
1	7150000016		NG	X6,CHKSCHD
	20540		SX5	PF.DSCHD
	11556		LX5	60=12
2	0315000006 *		BX5	X5+X6 . SEE IF ANY CRITICAL BITS ARE ON
	43561		NZ	X6,CHKSCHD . STILL NOT SCHEDULABLE
	20574		MX5	I
3	12656		LX5	60=PF.P . SET PENDING ACTION FLAG FOR SWAPPER
	20572		BX6	X5+X6
	12656		BX6	PF.P+60=PF.R . SET RUNNING FLAG
	54600		SA6	X5+X6
4	0120000001		WECS	A0
5	20652		LX6	I . WRITE UPDATE P.ROHEAD
	73160		SX1	60=18
	0400000000 X	CHKSCHD1	E0	X6 . PROCESS NOT FOR SCHED
6	54600		SA6	EXSCHED . SCHEDULE PROCESS AND EXIT
7	0120000001		WECS	A0
10	0270000000		JP	I . WRITE NEW P.ROHEAD
				R7 . EXIT

* THIS ROUTINE SEARCHES FOR A SUBPROCESS WITH INTERRUPT PENDING WHICH HAS PRIORITY OVER THE CURRENT SUBP FROM THE TOP OF THE STACK

* A PENDING INTERRUPT FOR THE CURRENT SUBPROCESS WILL NOT BE REPORTED IF THE INTERRUPT INHIBIT BIT IS SET IN THE STACK.

* AT ENTRY

B7 = RETLINK
B1 = PROCESS SCRATCH AREA

* AT EXIT

B2 = 0, IF NONE
B2 = A(SUBP DESCRIPTOR), REL BT, OF NEAREST ANCESTOR WITH INTERRUPT PENDING

* PRESERVES B5

ENTRY	PRIRINT	
ECSSUB	PRINT,BUF4	
SA2	B1+P.INTERR	: CHECK IF WE NEED ROTHER
SB2	0	: INITIALIZE B2
ZR	X2,PRIRINT2	: RETURN IF NO PENDING INTERRUPT

* SET A2= CURRENT SUBPROCESS AND BEGIN SEARCH

SA1	B1+PSTACK	: FETCH POINTER TO CURRENT TOS
SB2	X1+1	: PTR TO WO 1 OF STACK ENTRY
SB3	SD,FINT	: SHIFT VALUE OF INTERRUPT PENDING BIT
WDL	VFO	: 24/0:END OF PATH SP: 18/CURRENT SP

* THE FIRST WORD OF THE SUBPROCESS DESCRIPTOR CONTAINS THE INTERRUPT BIT AND THE FATHER POINTER

* SD,FINT VFO : 6/FLAGS: TA/INTERRUPT DATUM: 18/CLIST LENGTH: 18/FAT

SA2	B1+B2	: GET PTR TO CURRENT SUBP
SA1	B1+X2	: WORD 1 OF CURRENT SUBP
LX2	B3,X1	
PL	X2,PRIRINT3	: NO INT FOR CURRENT
SAZ	A2+1	: GET WORD 0 OF STACK
LX2	SF,II	: SEE IF INTERRUPTS ARE INHIBITED
PL	X2,PRIRINT2	: NO
SA1	A1+SD,PTRS	: GET FATHER POINTER
LX1	60-18	
SB2	X1	
EQ	B2,B0,PRIRINT2	: WERE AT THE ROOT
SA1	B1+B2	: WORD 0 OF FATHER

PROCESS STUFF
LOOK FOR PRIORITY INTERRUPT

22231

460 0322000456
461 0270000000
462

PRIPRINT2
11 + PRIPRINT

LX2
PL
JP
ENDSUB
EQU

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FPROC

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B3,XI
X2,PRIPRINT3
B7 .. RETURN WITH B2 SET APPROPRIATELY
PRINT,BUF#
PRINT

PROCESS STUFF
HANDLE ARITH ERRORS AND CHIP CALLS

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34
54 0000000000
55 0000000000
56

ENTRY S.CHIP,S.ARITH,CHIPCAL,ARITHERR
*
* XPACK FOR RA+1 NOT 0
*
S.CHIP BSSZ 16
*
* XPACK FOR ARITH ERROR (P-COUNTER = 0)
*
S.ARITH BSSZ 16
CHIPCAL PS
ARITHERR PS
END

36354 STORAGE USED 2305 STATEMENTS 475 SYMBOLS
6600 ASSEMBLY 11.841 SECONDS 715 REFERENCES

PROCESS STUFF
SYMBOLIC REFERENCE TABLE.

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AB,CPAVL	10		20/29	24/42				
AB,CPUSD	11		20/40					
AB,SIZE	13		20/38	21/18				
APT,AB	66		20/11	L	20/33	20/53	23/36	23,51
APT,D	72		20/13	L	20/27	23/26		
APT,P	70		20/12	L	20/50	20/52	23/56	
ARITHERR	55	PROGRAM*	43/01	E	43/11	L		
ARMIT	51		32/09	E	32/12	L		
ARMIT2	53		32/12		32/18	L		
ARMIT3	60		32/20		32/30	L		
AT,PROC	1		11/08					
BIGIX	0	EXTERNAL*	17/08					
BIGPAR	0	EXTERNAL*	17/03		17/04		17/05	38/27
BIGPT	0	EXTERNAL*	17/06		36/18		36/19	
BIG1A	74		35/21		36/17	L		
BIG2A	75		35/24		36/18	L		
BLMTSS	0	EXTERNAL*	17/10					
BUFA	460		41/22		42/05		42/05	
CAPAB	0	EXTERNAL*	11/15		12/01			
CHIPCAL	54	PROGRAM*	43/01	E	43/10	L		
CHKPTR	0	EXTERNAL*	37/34					
CHK\$CHD	0	PROGRAM*	21/43		40/15	E	40/16	
CHK\$CHD1	6	PROGRAM*	40/18		40/22		40/33	L
CLASCODE	66		33/29	D	33/33			
CLEN	56		6/35	D	7/08	S	13/02	14/32
CLIIB	51	EXTERNAL*	29/14	E	29/17	L		
CLMOT	0		17/09					
CLRMG	51		26/12	E	26/16	L		
CLRMG2	57		26/28	L				
CLRMG3	65		26/17		26/37	L		
CMRUFF	50		6/45		23/26		27/27	
			17/12		25/05		27/51	
			20/27		26/14		28/02	
			22/02		26/39		28/04	
			6/41	D	11/38	S	29/16	
			6/39	D	6/29		31/04	
			23/24	E	23/26		31/04	
			23/51		25/03		32/11	
			24/18	L	24/26		32/32	
			24/16		24/23		34/56	
			24/66		24/30		35/02	
			24/19		24/42		39/02	
			23/28		24/53	L	36/21	
			23/37		25/01		36/04	
			25/02	L				
			20/35	E	20/27	L		
			20/51		21/49			
			20/43		21/50	L		
			20/53		21/51	L		
			21/25	L	21/31		21/32	21/42
			21/20		21/21		21/31	
			20/58		21/47	L		
			20/34		21/48	L	23/46	

PROCESS STUFF
SYMBOLIC REFERENCE TABLE.

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			6/38 D	8/16 S	12/20	13/28	15/51			
DESCSIZ	61									
DISASTR	0	EXTERNAL*	16/42							
DISIT	51		33/27 E	33/32 L						
DISIT2	53		33/32	33/39 L						
DISIT3	61		33/50	34/14 L						
DISIT4	62		34/14	34/19 L						
DISITS	70		33/41	34/47 L						
DISIT6	71		34/19	34/48 L						
DISPST	51		35/11 F							
DSSTE	51		37/27 E	37/30 L						
DSSTE1	54		37/33	37/38 L						
DSSTE3	64		37/46	38/19 L						
DSSTE4	65		37/52	38/20 L						
DSS2	63		35/38	35/40 L						
DSS3	65		35/48	36/13						
EC\$AD	63		6/40 D	11/14 S	13/40					
ENDSYS	0	EXTERNAL*	8/23							
E.ARMIT	424	EXTERNAL*	32/10 L							
E.CLIIB	415	EXTERNAL*	29/15 L							
E.CLRMG	355	EXTERNAL*	26/13 L							
E.CPOUT	302	EXTERNAL*	23/25 L							
E.CPROC	0	EXTERNAL*	6/44 L							
E.CPUIN	227	EXTERNAL*	20/26 L							
E.DISIT	434	EXTERNAL*	33/28 L							
E.DLPRO	414	EXTERNAL*	28/01 L							
E.DPROD	423	EXTERNAL*	31/01 L							
E.DSPST	455	EXTERNAL*	35/01 L							
E.DSSTE	503	EXTERNAL*	37/28 L							
E.ECS	0	EXTERNAL*	6/52	16/09	20/39	21/02	23/37	24/14	26/17	27/48
			7/06	16/22	20/51	21/12	23/46	24/19	26/34	40/30
			13/44	16/32	20/53	21/10	23/51	24/45	27/29	40/35
			14/20	20/34	20/55	21/37	23/51	24/49	27/33	42/65
E.ERROR	0	EXTERNAL*	15/01							
E.NOERR	12	EXTERNAL*	41/21 L							
E.PRINT	21	EXTERNAL*	39/01 L							
E.PTINT	320	EXTERNAL*	30/08 L							
E.STIIB	420	EXTERNAL*	27/26 L							
E.STMSG	372	EXTERNAL*	21/52 L							
FATSON	0	EXTERNAL*	25/04							
FINDSUB	0	EXTERNAL*	33/34							
I.AWDS	2	EXTERNAL*	23/51							
I.LOCK	0	EXTERNAL*	21/05 S	24/05 S	24/50 S	26/26 S	26/34 S	27/41 S	27/46 S	
L.ARMIT	10	EXTERNAL*	32/32 D							
L.CLIIB	3	EXTERNAL*	29/27 D							
L.CLRMG	15	EXTERNAL*	26/39 D							
L.CPOUT	33	EXTERNAL*	25/05 D							
L.CPROC	227	EXTERNAL*	17/12 D							
L.CPUIN	53	EXTERNAL*	22/02 D							
L.DISIT	21	EXTERNAL*	34/50 D							
L.DLPRO	1	EXTERNAL*	28/04 D							
L.DPROD	1	EXTERNAL*	31/04 D							
L.DSPST	26	EXTERNAL*	36/21 D							
L.DSSTE	15	EXTERNAL*	38/22 D							

PROCESS STUFF
SYMBOLIC REFERENCE TABLE.

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MP.FILE2	22		6/26 L	9/26						
MP.FL	11		6/19 L	8/20	9/13	9/44	11/22	11/30	15/52	
MP.FLAD1	17		6/23 L	8/51						
MP.FLAD2	24		6/27 L	9/27						
MP.MAP	7		6/17 L	7/52	14/35	15/36	16/23			
MP.RAFL	1		5/20 D							
MP.SIZE	3		5/26 D	16/21						
MP.STACK	4		6/15 L	7/42	14/20					
MP.WDCT1	21		6/25 L	9/06						
MP.WDCT2	26		6/29 L	9/37						
NEGIX	0	EXTERNAL*	17/07							
NEGPAR	0	EXTERNAL*	16/44	16/46	16/48	16/50	16/52	17/01	21/48	36/26
			16/45	16/47	16/49	16/51	16/53	17/02	25/01	38/26
NEGPT	0	EXTERNAL*	36/17							
NEG1A	73		35/16	36/16 L						
NEG2P	76		35/29	36/19 L						
NOCP	0	EXTERNAL*	21/51							
NOFIND	0	EXTERNAL*	32/31	34/48						
NOROOM	0	EXTERNAL*	17/11							
PATCH1	52		23/27 L							
PATCH2	63		26/35 L							
PATCH3	67		27/46 L							
PATCH4	56		32/26 L							
PATCH5	57		34/06 L							
PATCH5.5	66		34/42 L							
PATCH6	71		36/14 L							
PF.C	6		3/43 D							
PF.CREA	4010		3/48 D	11/48						
PF.D	4		3/41 D							
PF.OSCHO	16		3/51 D	40/19						
PF.E	15		3/42 D							
PF.EE	11		3/46 D							
PF.H	12		3/47 D							
PF.I	13		3/49 D							
PF.KILL	4200		3/47 D							
PF.P	0		3/37 D	40/24	40/26					
PF.R	2		3/39 D	40/17	40/26					
PF.S	10		3/45 D	21/35						
PF.SWPN	6636		3/50 D							
PF.V	7		3/44 D							
PF.W	1		3/38 D							
PRINT	11	PROGRAM*	34/15	42/05 L	42/05					
PRIPRINT	11	PROGRAM*	41/20 E	42/05 D						
PRIPRINT2	461		41/24	41/48	41/52	42/03 L				
PRIPRINT3	456		41/45	41/49 L	42/02					
PS.MASKL	1		4/34 D							
PS.TEMP	1		4/31 D	4/33 D	4/33	4/34				
PUTCAP	0	EXTERNAL*	11/17							
P.APLL	35		3/07 D							
P.CLIST	162		4/16 L	10/07						
P.CLLOFF	0		5/13 D							
P.CTABLE	163		4/19 L							
P.ERRORFF	1		5/14 D							

**PROCESS STUFF
SYMBOLIC REFERENCE TABLE.**

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SD.ORIG	5		4/47 D	15/28 S		
SD.PTRS	4		4/46 D	15/18 S	41/49	
SD.RAFL	7		4/51 D	16/06 S		
SD.SIZE	10		4/40 D	8/03	14/41	16/08
SF.FINT	1		5/43 D			16/14
SF.II	0		5/42 D	14/04	29/20	30/11
SF.PCQ1	4		5/44 D	14/04	14/06	34/27
SF.PCQ2	5		5/45 D	34/30		34/33
STACKO	57		6/36 D	7/22 S	12/19	13/07
STIIB	51		30/67 E	30/10 L		
STK.CMAD	66		37/15 L	37/31		
STK.INDX	67		37/17 D	37/42		
STK.WDCT	67		37/16 L	37/17		
STM5G	51		27/25 E	27/28 L		
STMSG3	71		27/29	27/48 L		
STMSG4	72		27/33	27/49 L		
SURPT	60		6/37 D	7/48 S	13/15	14/17
SYSFRET	0	EXTERNAL*	34/48			
SYSRET	0	EXTERNAL*	16/33	24/52	27/47	32/28
			21/26	26/36	30/18	34/07
S.ARITH	34	PROGRAM*	43/01 E	43/09 L		
S.CHIP	14	PROGRAM*	43/01 E	43/05 L		
S.CMFL	0	EXTERNAL*	8/25	12/42		
S.FNFLG	2		5/36 D			
TOSPROC	0	EXTERNAL*	29/53	34/43		

13.17.44. 11/15/71 SCOP32D OF 08/27/71
13.17.53.\$: CM=18432/0440000 AT CP= 0 SEC
13.17.58.NOMPASS,I=PROCESS,S=0
13.19.12. ASSSEMBLY COMPLETE.
13.19.21.END
13.19.23.FIN
13.19.24.\$: USER CPU = 12.579 SEC
13.19.24.\$: SCOPE CPU = 5.152 SEC
13.19.25.\$: SCOPE ECS = 5.769 SEC
13.19.25.\$: SCOPE SWAP = 14.482 SEC
13.19.25.\$: DISK CPU = 4.591 SEC
13.19.25.\$: DISK ECS = 2.222 SEC
13.19.26.\$: DISK SWAP = .949 SEC
13.19.26.\$: SYSTEM = 2070 LINES

13°22.21° 2083 LINES PRINTED BY PRINTER nRIVER ON LP 2.