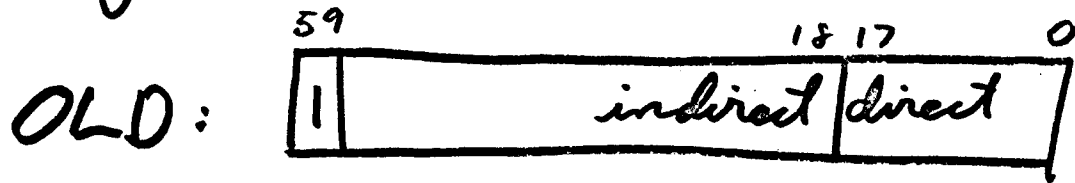


20 July '70

Change to indirect C-list specifications:



direct is the index of a capability for a C-list in the full C-list.

indirect is the index of the desired capability in the C-list specified by direct.

20 July '70

Very preliminary specification of block data & block capability parameter types:

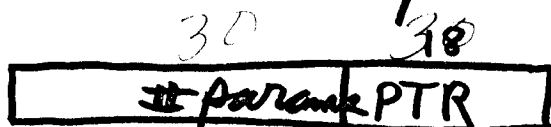
D. 6. Change parameter from "none" to "block ~~capability~~ data"

IP1 C: Cap for operation (OB.CH TYP)
IP2 D: parameter index
IP3

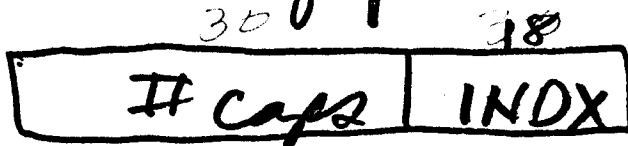
D. 7. Change parameter from "none" to "block capability"

IP1 same as D. 6.
etc

Form of block data parameters in IP-list:



Form of block cap parameters in IP-list:



20 July '70

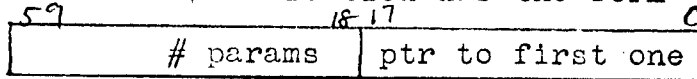
Preliminary description of return with parameters operation; will go on about page 34 of USER MANUAL:

M. Return with Parameters

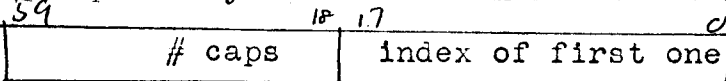
IP1 D: Parameter specification
IP2 D: Capability specification

~~XXXXXXXXXX~~
~~XXXXXXXXXXXXXXXXXX~~

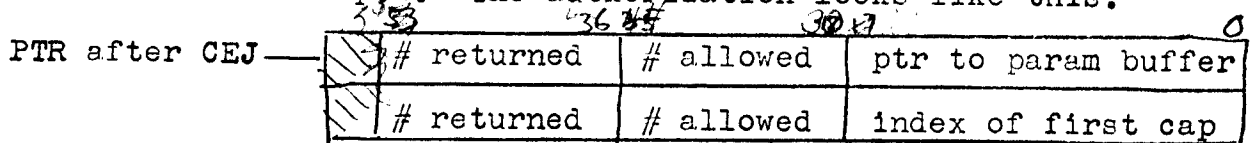
The parameter specification has the form



and the capability specification has the form



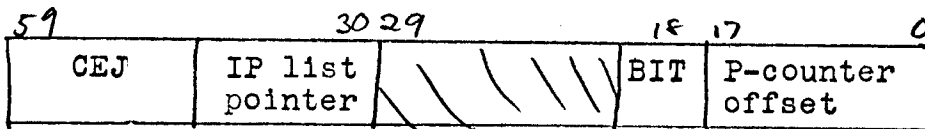
Parameters from the full ~~XXXXXXXXXX~~ address space and capabilities from the full C-list of the subprocess requesting the action are returned to the full address space and full C-list determined by the top of the stack. Provided, of course, that all pointers, counts, indices, etc., in both subprocesses are legal. In particular, the ~~CXIXIX~~ P-counter in the stack must point to a CEJ which indicates a return authorization as ~~XXXXXXXX~~ indicated on p.4. The authorization looks like this:



Both "# returned" fields are set by the operation to the actual number returned. Attempting to return more than the authorization allows is not an error.

The error list associated with this operation is not yet compiled.

Alteration to p.4 of USER MANUAL:



BIT = $\begin{cases} 0, & \text{if no return authorized} \\ 1, & \text{if return authorized} \end{cases}$

When BIT = 1, the word following the CEJ must contain the return authorization PTR:

