

Information

# MARK III

ptr types

0 ≡ SD. <del>FIPT</del>	FLAGS	INTERRUPT PENDING	C-LIST LENGTH	MAP IN	?	0
SD.CC	SUBPROCESS NAME (CLASS CODE)					
SD.ESM	ERROR SELECTION MASK					
SD.CLST	C-LIST UNIQUE NAME, MOT					
SD.PTRS	MAX STACK PTR	FATHER PTR	MAPIN LIST PTR	MAPIN LIST PTR	0 = not in	3 2 X
SD.ORIG	ENTRY POINT	MAP ORIGIN	C-LIST ORIGIN	C-LIST ORIGIN		always 0.
SD.MAP	COMPILED MAP SIZE	# LOGICAL MAP ENTRIES	PTR TO LOGICAL MAP, REL THIS WORD			
SD.MAP+1 ≡ SD.RAFL	RAFL	RA	SPACE LEFT IN COMPILED MAP BOP			1 1 0

compiled map follows immediately

Δ - applies uniformly to all 3 ptrs unless noted

- 0 no relocation points to next SP, rel to beg the SP
- 1  $V_1 + V_2 + V_3 + V_4$  ; rel B1
- 2  $V_3$  ~~constant~~ ; rel to beg this SP; change when they ~~point across a deletion~~ <sup>B1</sup>   
  $V_1 + V_2 + V_3 (+V_4)$  <sub>sometimes</sub>
- 3  $V_1 + V_2 + V_3$  ; rel B1

FLAGS      SD.FINT      interrupt pending  
              SD.FMPE      map error pending  
              SD.FOIS      interrupts disarmed  
              ~~SD.MPIN~~      ~~map in~~

F=1 if map is off



accumulated changes to  
process descriptor

P. SCHED gets bigger

~~needn't go out to ECS~~

S. INTIM near clocks: set to S. CHARG on swapin

contiguous with usrtimo, etc.

P. TIMER - - decremented by S. CHARG - S. INTIM at swapout; if -, desched

P. MMSG UN, MOT of each  
message (event)

P. CLOX = number of clocks in user process (= 4)

P. MAPESM disappears

P. IRLIST disappears  
P. OLDP " " " "

Flags:

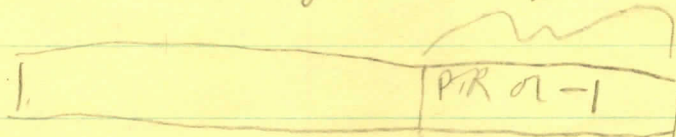
- PF. P <sup>something</sup> pending for swapper
- PF. W unchain from evch
- PF. R scheduled (running)
- ~~no longer necessary~~ PF. I pending interrupt
- PF. D pending destruction of process
- 5 ≡ PF. E 0 = real process, 1 = pseudo process
- PF. C process in core
- PF. V rescheduled by arrival of event?
- PF. S descheduled by timer out
- ↑ PF. EE " for over event

all these names are only for cross-referencing

PF. H ~~being~~ <sup>descheduled</sup> cause hung on EVCH

~~P. TEMPL~~

R. MARSIN



(set to -1 ~~desched~~ by swapper at swapin)

P.PARAM

data word in 1 cell containing the data

X

block is 2 words, 1st word = 0  
2nd word =

cap is 2 words, 1st

-2

parameter type bit mask

0 = data word  
1 = other, dep on format

-1

words used in	# cap	total data
A para	para	para

P.PARAMC

class code of called subp

# STACK ENTRIES

FLAGS	F-RETURN COUNT	IP LIST ADDR, REL RA OF CURRENT	P-COUNTER, REL RA OF CURRENT	← PTR
+		END OF PATH SUBPROCESS <sup>1</sup>	CURRENT SUBPROCESS <sup>1</sup>	

FLAGS: SF.II - Interrupt inhibit. If an interrupt arrives for the current subprocess (when it is at the top of the stack & has interrupts armed) & this bit is set, the interrupt ponds (or is lost if an interrupt is already pending) until the bit is cleared.

The bit is automatically set on any subprocess call; it can be explicitly set & cleared by the current subprocess; when it is cleared any pending interrupt will occur immediately.

SF.PCQ1 ≡ 4 The p-counter qualifier. These bits must be adjacent to the freturn count & are named merely for cross-referencing purposes. Values are:

SF.PCQ2 ≡ 5

<sup>1</sup> These fields are pointers to the first word of the appropriate subprocess descriptors. The pointers are relative to B1.

SF.PCQ1 = 1 = about to execute  
 SF.PCQ2 = 1 = almost finished  
 both = 0 = in middle  
 both = 1 should not occur

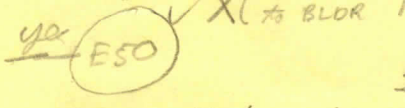
flow charts

# ENVIORN

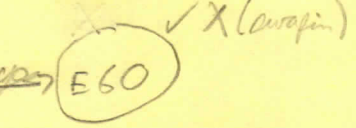
initialize reg, etc (not B6, MCSP!!)

- 1) end of map chain = -SD.PTR
  - a) in ENVIORN ✓
  - b) in SWAP
  - c) OMYCOD
- 2) rdMA in 1) OMYCOD
  - 2) Create process

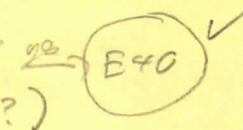
EOP already in? yes



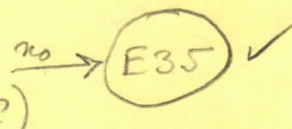
end of map chain? yes



EOP above MCSP? yes  
(EOP RA  $\geq$  MCSP RA + FL?)

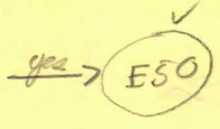


EOP below MCSP? no  
(EOP RA + FL  $\leq$  MCSP RA?)

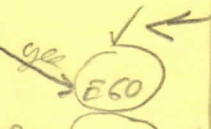


PMCSPP ← MCSP

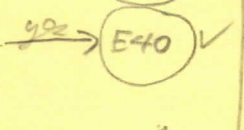
E20 FPSP already in? yes



E30 end of map chain? yes



FPSP above MCSP? yes



E35 set MCSP not in core  
PTR in PMCSPP ← PTR from MCSP

MAPOUT(MCSP)

file gone error? no

↓ yes  
(needshely!)  
flag sdMCSP for pending map error

1 ref (EOP collide with MCSP)



E40 make PMCSP point to FPSP;  
FPSP point where PMCSP did

MAPIN(FPSP)

remember error if 1) map off  
2) file gone  
3) map over pending

FPSP above MCSP  
(2 places)

E20

E50 PMCSP ← FPSP  
make entry in full child for FPSP,  
B3 ← B3-2

FPSP = CURRENT? → yes → E11 ✓  
↓ no

FPSP ← father of FPSP

FPSP already is  
(2 places)

E60 → set PMCSP pointer to FPSP

PMCSP ← FPSP  
MAPIN(FPSP)

remember error if 1 2 3 as above

make entry in full C-list;  
B3 ← B3-2

FPSP = CURRENT? → yes → E70 ✓

FPSP ← father of FPSP

end of map chain  
(2 places)

E70 set end of map chain flag in FPSP ←

E71 set S.USERA to absolute userRA ←

set userRA (abs.) in XPACK

set FL in XPACK = RA+FL of EOP - RA of current  
{set MA in XPACK} ← being ripped out

set P.CLIST

set up direct ECS access if any

restore return links

read local clist (if gone) → E80

map error? — yes

↓ no  
exit to B7 (normal)

E74 exit to B6 with MAPER error

E80 map error? — yes

↓ no  
CGONE error exit to B6

BOTH error to B6 ←



New TOs

~~1) Is it Subproc fixed?~~

P. TEMP4 ← rethink

substack pin > current stack?  $\xrightarrow{\text{no}}$  stack  
full error  
 $\downarrow$  yes

# TOSPROC

SYSPRET

increment return count in stack  
 $FRET(TOS) \leftarrow FRET(TOS) + 1$

SYSPRET  
 $RET \leftarrow 1$

TOSPROC

$RET \leftarrow 1$  (off)

Priority int?  $\xrightarrow{yes}$

NEWTOS (interrupt subp)  
 TOSPROC

TOSPR2

TOS about to exp?  $\xrightarrow{yes}$

TOSPR1 newPC  $\leftarrow PC(TOS)$

TOS almost fin?  $\xrightarrow{yes}$

Ret gas?  $\xrightarrow{yes}$  check this  
return ... error  
than

TOSPR4

$FRET(TOS) > 0$ ?  $\xrightarrow{yes}$  check this  
FRET

TOSPR7

Fetch XJ  
 get offset  
 is XJ?  $\xrightarrow{no}$  error

TOSPR8

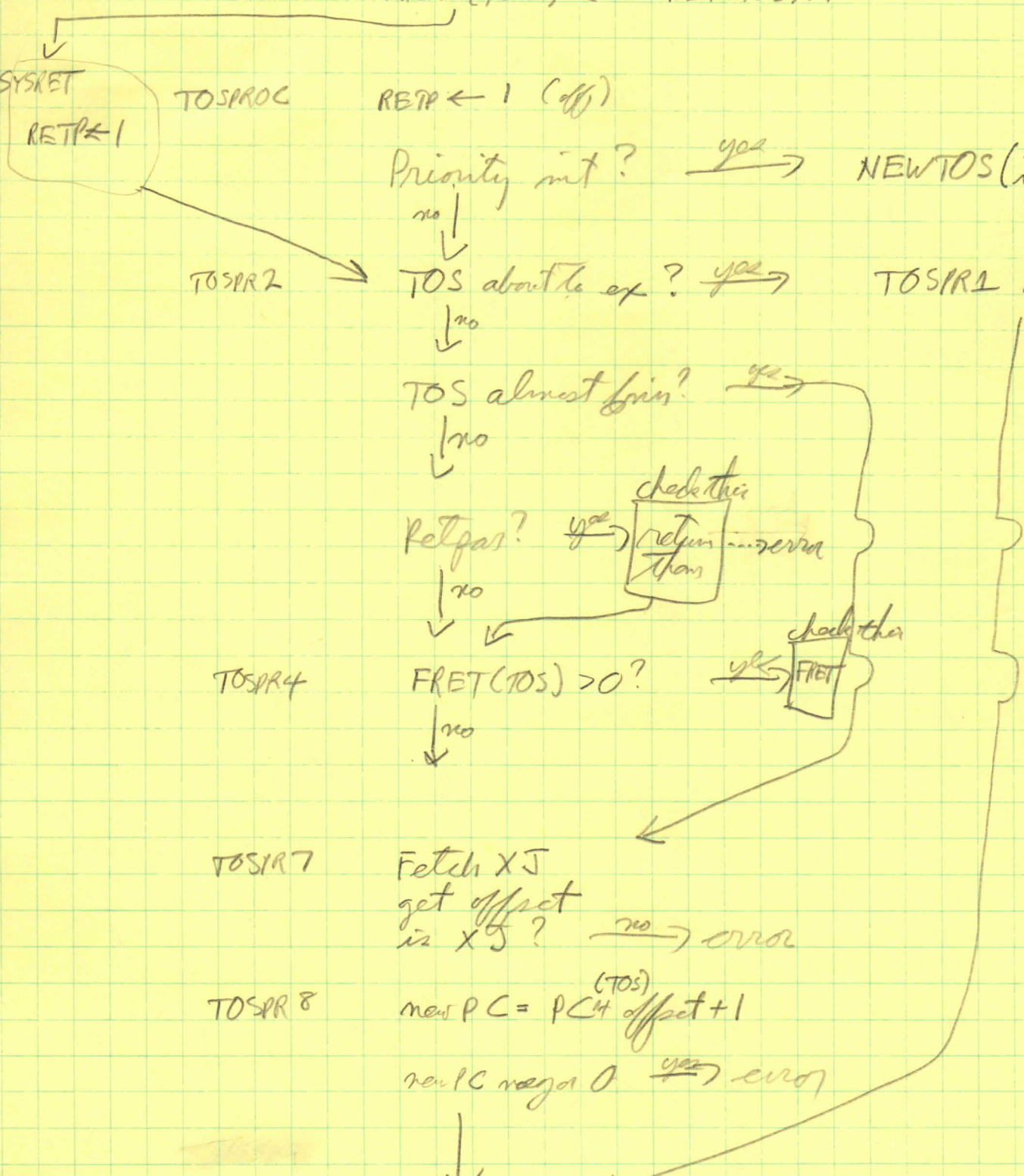
newPC =  $PC^{(TOS)}$  offset + 1  
 newPC nega 0  $\xrightarrow{yes}$  error

TOSPR2.5

$PC(XPACK) \leftarrow newPC$

update P.SYSTIM, S.SYSTIM  
 display user  
 XJ

S.RETU



# USERCAL

display system

update P.USRTIM, S.USRTIM

fetch PCCXPACK) = CEJ

$B4 = \text{low 16 bits} < 0?$   $\xrightarrow{\text{yes}}$   
 $\downarrow$   $\text{no}$   $\xrightarrow{\text{no}}$   $B4 = -\text{last 4 bits}$

UCALL1 TOS  $\leftarrow$  deflags, FRST=0, IPLIST, PC

IPLIST  $< 0$  or  $\geq FL$   $\xrightarrow{\text{error}}$

Decap (IPO)

UCALL2 IPO = open  $\xrightarrow{\text{no}}$  error

IPO still in use?  $\xrightarrow{\text{no}}$  error

P.SCR2  $\leftarrow$  open header & mask bits

B1  $\leftarrow$  rest of open

ip list beyond PL  $\xrightarrow{\text{no}}$  error

parameterless?

initializing PARAMBUF, actual parameters area, etc.

OPINTER

SA2 B1+3 = class code

Subgoal?

$\downarrow$  no  
action

yes

act

P.PARAMC

-1  
-2

} class code  
bitmask

action

# ERRPROC

E.ERROR

RET LINK ← TOSPROC

ERRPROC

P.PARAM ← err #

stack full  $\rightarrow$  KILLPROC

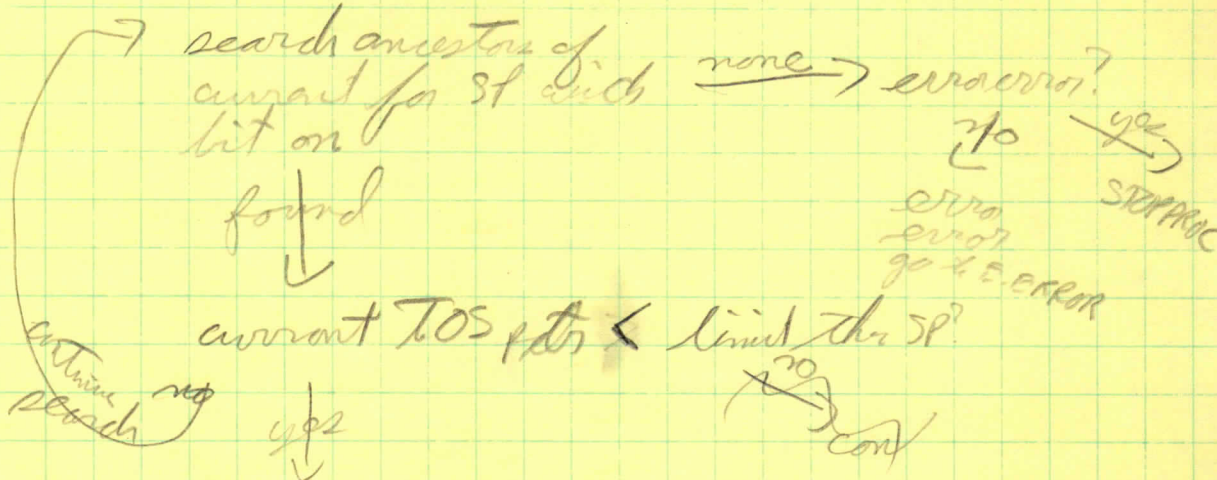
P.PARAM+2 ← current SP ptrs from TOS

B6 ← top of page " "

err class 360

B3 ← err class

error too big  
go to E.ERROR



Turn off bit

P.PARAM+1 ← err class

entry offset = P.ERROROFF

go to NEWTOS

# PROCESS

C E X T

Create	✓	✓	✓	
Delete				
Display				
Setmess	G	✓		
Clr mess	G	✓		
Timin	G	✓	✓	
Timout	G	✓		
Actib (ST11B)	G	✓		
Clrib (CL11B)	G	✓		
Arus	G	✓		
Pisorn	G	✓		
Print (PRINT)	G <sup>uv</sup> (2ang)	G		
<del>scpr</del> with				
CHIP				
Display stack	G			
" " entry	G			
Send interrupt PTINT	●			

# SUBPROC

# C E X T

create	✓	✓	✓
Delete	✓	✓	✓
Jumpcal	✓	✓	
Call FANCY	✓	✓	✓
Return	✓	✓	✓
SPRET	✓	✓	✓
Retparm	✓	✓	✓
Jumpret			
FINDMONTHS	✓	✓	✓
MODPC	✓	✓	
FSON	✓	✓	
DSPSP	✓	✓	
FRETURN	✓	✓	✓
ERRCALL	✓	✓	
ESMSETH (ESMSN)	✓	✓	
ESMSET (ESMS)	✓	✓	
FINDSUB	✓	✓	
ENVIORN	✓	✓	
Setretq	✓	✓	✓



SYSENT

C

E

X

T

TOSPROC

✓

✓

✓

PUTRETP

✓

✓

✓

OPINTER

✓

✓

✓

SYSRET

✓

✓

✓

SYSFRET

✓

✓

✓

E.ERROR, ERRPROC

✓

✓

✓

NEWTOS

✓

✓

✓

## MAPS

C E X T

MAPIN	✓	✓	✓	
MAPOUT	✓	✓	✓	
MAPCHK	✓	✓	✓	
CALLCMP	✓	✓	✓	
REFER	✓	✓	✓	
REFZ	✓	✓	✓?	
DISPLAY (Current)	✓	✓		
" (Subj)	✓	✓		
MAPON				
MAPOFF				

## SWAP

SWAPOUT	✓	✓	✓	
SWAPIN	✓	✓	✓	
FSWAP	✓	✓	✓	

# Routines

- ✓ TOS Processor (except glue in return parameter code)
- ✓ SYSRET
- ✓ New TOS (m, descriptors of callee)
- ✓ Environ (TOS) ~~(has initialization & some writeup)~~

~~Locate BP~~

- ✓ FINDSUB : X3 = classcode  
B7 = retlink
- ✓ MAPIN ✓ ~~BS = 1 or preserved?~~

✓ MAPOUT

✓ CALLCMP

✓ REFER

✓ REFZ

✓ MAPCHK

✓ Display map entry of SP

✓ " " " from fall path

✓ SWAPOUT ~~(all but set up stack) (in Forward)~~

✓ SWAPIN (all but destroy process, separate out ENVIRON error)

## Fetch parameters

✓ Return parameters

✓ SYSCALL

✓ SYSFRET  
Return opor

✓ BP  
✓ E.ERROR

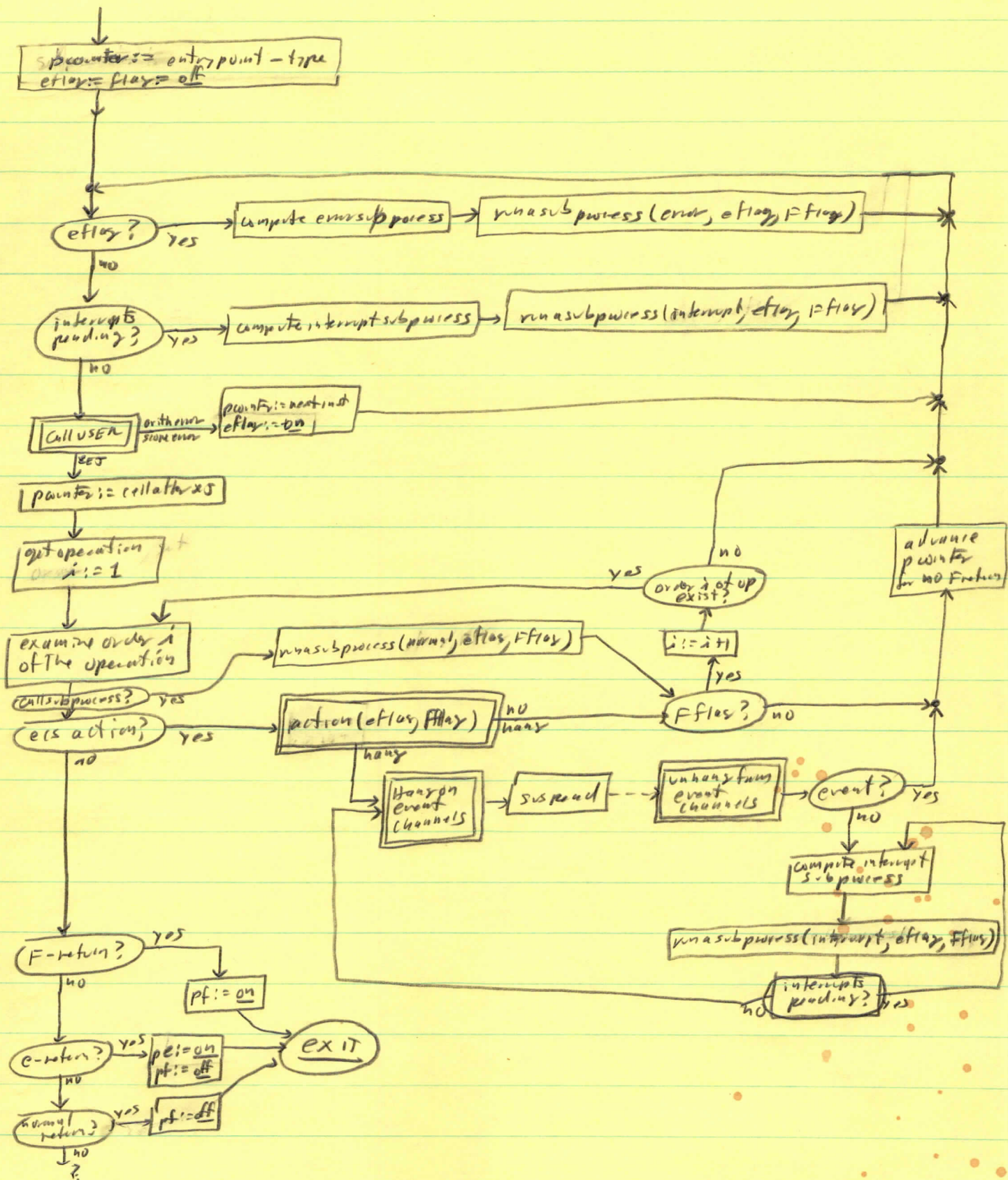
- SCOPE error
- ✓ CHIP error
- ✓ Subprocess call
- ✓ Outprocess jump call

Old stuff.\*

\* (junk)

6/9/70

run a sub process (type, pe, pf) local etlag, fflag, pointer, i



NEWTOS

<sup>call type</sup>  
<sup>ldll</sup>  
<sup>B3</sup>  
New TOS <sup>descriptor</sup> <sup>B2</sup> <sup>B7</sup> <sup>retlink</sup>  
<sup>n</sup> <sup>class code of called</sup> SP, <sup>retlink</sup>  
stack full error?  $\dots \rightarrow$  EP <sup>B6</sup>

determine EOP, set EOP, current  
increment stack stuff  
set PCN = about to  
f-ret = 0  
p-counter = EP - n  
interrupt inhibit

error retlink

Environment (TOS)  $\dots \rightarrow$  EP

~~if~~  
if non-trivial full path, set low core  
with data relevant to old current SP.

n = 0

params from A area  
 $\dots \rightarrow$  EP

n = 1

params from  
error drop area

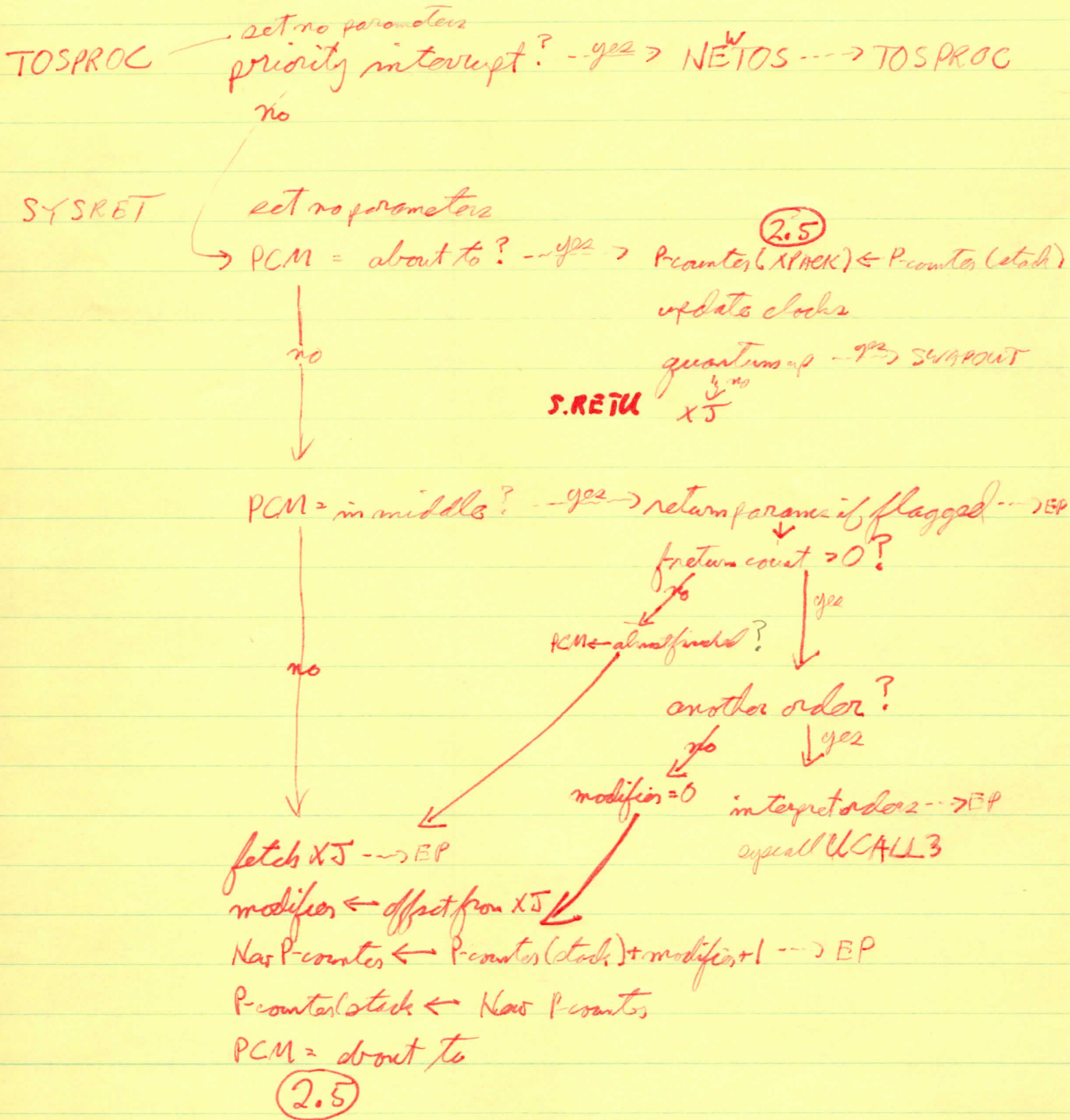
n = 2

int datum  
from  
current SP  
or some  
drop  
area

return

must  
will not return if errors are encountered

# TOS Processor - Execution (TOS) exists



~~#100~~ ~~#100~~

# System Call clocks

set 1st word of top of stack (PC = 14 middle)

P-counter = STACK-1

f-returns = 0

~~set exit from OPINTER = UCALL3~~

fetch IPO, set up 1st order --> EP

opinter xit to UCALL3

OPINTER --> EP

UCALL3 essentially jump thro ACTION

ACTION --> EP

jump  
SYSRET  
SYSRET

entered after processing orders by F return code

~~UCALL3~~ ~~advance to next order~~

~~order exhausted~~

order count = f return count --> yes: UCALL3

another order --> yes: XX after reading order

Return



## get event

event  $\xrightarrow{\text{yes}}$  system return  
 $\downarrow$  no

deschedule process

swapout

## set IIB

## clear IIB

test for pending interrupt

## Swapout

FORSWAP - fix clocks  
set up TOS  
change X clocks around

## Swapout

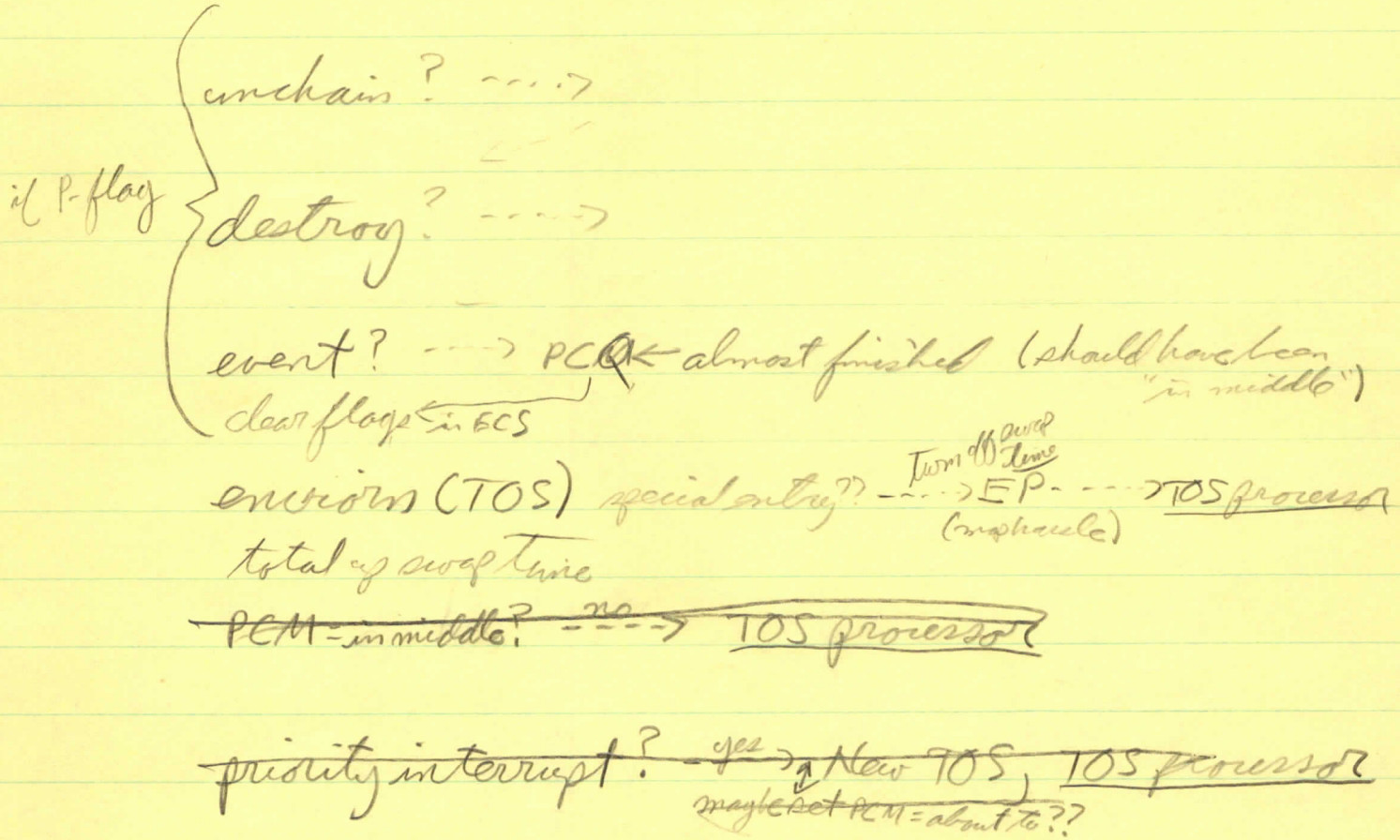
get next process from scheduler  
swapout maps (flag subps for any map errors)  
write out process descriptor  
timer out?  $\xrightarrow{\text{yes}}$  deschedule process  
write out clocks  
go to swapper

TOS for process being swapped in is always "about to execute" unless it was being on an event channel in which case it is "in the middle".

swapper

Initialize S.INTIM, S.QUANT

Read process descriptor



~~we do not have to rehang a guy because the interrupt which woke him up didn't have priority~~

~~PCM = about to~~

~~TOS processor (swap flag)~~

pending? <sup>no</sup> ----> S.RETU

PCQ = in middle? <sup>yes</sup> ----> PCQ ← about to  
<sub>no</sub>

TOS PROC

EP  
E.EKKOM  
ERRPROC  
return = TOS processor  
locate SP to field error -----> NO SUMP ERR ----> EP  
turn off bit in ESMK  
↓  
deschedule  
& swapout  
~~Stack up has, and ep in new~~  
~~Top of stack~~

overruns to <sup>processor</sup> ~~EP~~ area  
~~calls sub(1)~~  
PCN = about to      New TOS  
~~f-counter = entry - 1~~  
~~f-return count = 0~~  
~~calls sub(1)~~  
return

SP call action  
locate called SP ----> EP  
New TOS (1) -> TOS processor  
SP jump call  
locate callee ----> EP  
dec stack  
New TOS (1) -> TOS processor